

## EOLX-BI1680-16ADL & EOLX-BI1680-16DAL

Tx: 1270nm/Rx: 1330nm BIDI XFP Transceiver

Tx: 1330nm/Rx: 1270nm BIDI XFP Transceiver

RoHS 6 Compliant

### Features

- ◆ Supports up to 8.5Gb/s
- ◆ Power budget 16dB at least
- ◆ Two types:
  - A: 1270nm DFB Transmitter/ 1330nm Receiver
  - B: 1330nm DFB Transmitter/ 1270nm Receiver
- ◆ LC Connector
- ◆ +3.3V power supply only
- ◆ Power dissipation <2.5W
- ◆ Built-in digital diagnostic functions
- ◆ Case temperature range:
  - Standard: 0~+70°C
  - Extended: -10~+85°C



### Applications

- ◆ 800-SM-LL-L Fiber Channel
- ◆ 4G/2G/1G Fiber Channel

### Ordering information

Part No.	Data Rate	Laser	Temp.	Distance	Optical Interface	DDMI
EOLX-BI1680-16ADL <sup>*note1</sup>	8.5Gbps	1270nm DFB	Standard	40km	LC	YES
EOLX-BI1680-16DAL <sup>*note1</sup>	8.5Gbps	1330nm DFB	Standard	40km	LC	YES
EOLX-BI1680-16ADIL	8.5Gbps	1270nm DFB	Extended	40km	LC	YES
EOLX-BI1680-16DAIL	8.5Gbps	1330nm DFB	Extended	40km	LC	YES

\*Note1: Standard version.

## Regulatory Compliance\*

Product Certificate	Certificate Number	Applicable Standard
TUV	R50135086	EN 60950-1:2006+A11+A1+A12
		EN 60825-1:2007
		EN 60825-2:2004+A1+A2
UL	E317337	UL 60950-1
		CSA C22.2 No. 60950-1-07
EMC CE	AE 50285865 0001	EN 55022:2010
		EN 55024:2010
CB	JPTUV-049251	IEC 60825-1
		IEC 60950-1
FCC	WTF14F0514437E	47 CFR PART 15 OCT., 2013
FDA	1331340-000	GDRH 1040.10
ROHS	RHS01G006464	2011/65/EU

\*The above certificate number updated to June 2014, because some certificate will be updated every year, such as FCC, FDA and ROHS. For the latest certification information, please check with Eoptolink.

## Product Description

The EOLX-BI1680-16-XXX series single mode transceiver is small form factor pluggable module for duplex optical data communications. It is with the XFP 30-pin connector to allow hot plug capability.

The EOLX-BI1680-16ADX module is designed for single mode fiber and operates at a nominal wavelength of 1270nm; EOLX-BI1680-16DAX module is designed for single mode fiber and operates at a nominal wavelength of 1330nm. The transmitter section uses a multiple quantum well DFB, which is class 1 laser compliant according to International Safety Standard IEC-60825.

The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

## Absolute Maximum Ratings<sup>\*note2</sup>

Parameter	Symbol	Min	Max	Unit
Maximum Supply Voltage	V <sub>cc</sub>	-0.5	4.0	V
Storage Temperature	T <sub>s</sub>	-40	85	°C
Case Operating Temperature	T <sub>c</sub> , EOLX-BI1680-16ADL	0	70	°C
	T <sub>c</sub> , EOLX-BI1680-16ADIL	-10	85	

\*Note2: Exceeding any one of these values may destroy the device permanently.

## Recommend operating condition

Parameter	Symbol	Min	Typ	Max	Units
Case Operating Temperature	Tc, EOLX-BI1680-16ADL	0	-	70	°C
	Tc, EOLX-BI1680-16ADIL	-10	-	85	
Power Supply Current	I <sub>cc</sub>	-	-	800	mA
Supply Voltage	V <sub>cc</sub>	3.13	-	3.45	V

## Electrical Characteristics

Parameter	Symbol	Min	Typ	Max	Unit
<b>Transmitter</b>					
Data Rate			8.5		Gbps
Input differential impedance	R <sub>in</sub>	90	100	110	Ω
Differential data input swing* <sup>Note3</sup>	V <sub>in,pp</sub>	120	-	820	mV
Transmit Disable Voltage	V <sub>D</sub>	2.0	-	V <sub>cc</sub>	V
Transmit Enable Voltage	V <sub>EN</sub>	GND	-	GND+ 0.8	V
Transmit Disable Assert Time		-	-	10	us
<b>Receiver</b>					
Differential data output swing* <sup>Note3</sup>	V <sub>out,pp</sub>	340	650	850	mV
Data output rise time* <sup>Note4</sup>	t <sub>r</sub>	-	-	38	ps
Data output fall time* <sup>Note4</sup>	t <sub>f</sub>	-	-	38	ps
LOS Fault	V <sub>LOS fault</sub>	2.4	-	V <sub>cc</sub>	V
LOS Normal	V <sub>LOS norm</sub>	GND	-	GND+0.5	V

\*Note3. Internal AC coupling.

\*Note4. 20 – 80 %.

## Optical Characteristics

(EOLX-BI1680-16ADX, 1270nm DFB & PIN/TIA)

Parameter	Symbol	Min.	Typical	Max.	Unit
Power budget		16			dB
Data Rate			8.5		Gbps
<b>Transmitter</b>					
Centre Wavelength	λ <sub>c</sub>	1260	1270	1280	nm
Spectral Width (-20dB)	Δλ			1	nm
Average Output Power* <sup>note5</sup>	P <sub>out, AVG</sub>	1		5	dBm
Extinction Ratio	ER	3.5			dB
Side Mode Suppression Ratio	SMSR	30			dB
Transmitter and Dispersion Penalty	TDP			2	dB
Average Power of OFF Transmitter				-30	dBm
Relative Intensity Noise	RIN			-128	dB/Hz
Input Differential Impedance	Z <sub>IN</sub>	90	100	110	Ω
TX Disable	Disable	2.0		V <sub>cc</sub> +0.3	V

	Enable		0		0.8	
TX Fault	Fault		2.0		$V_{CC}+0.3$	V
	Normal		0		0.8	
TX Disable Assert Time		$t_{off}$			10	us
<b>Receiver</b>						
Centre Wavelength		$\lambda_C$	1320		1340	nm
Sensitivity <sup>*note6</sup>		$P_{IN}$			-15	dBm
Receiver Overload		$P_{MAX}$	0.5			dBm
Output Differential Impedance		$P_{IN}$	90	100	110	$\Omega$
LOS De-Assert		$LOS_D$			-17	dBm
LOS Assert		$LOS_A$	-29			dBm
LOS	High		2.0		$V_{CC}+0.3$	V
	Low		0		0.8	

### (EOLX-BI1680-16DAX, 1330nm DFB & PIN/TIA)

Parameter	Symbol	Min.	Typical	Max.	Unit	
Power budget		16			dB	
Data Rate			8.5		Gbps	
<b>Transmitter</b>						
Centre Wavelength	$\lambda_C$	1320	1330	1340	nm	
Spectral Width (-20dB)	$\Delta\lambda$			1	nm	
Average Output Power <sup>*note5</sup>	$P_{out, AVG}$	1		5	dBm	
Extinction Ratio	ER	3.5			dB	
Side Mode Suppression Ratio	SMSR	30			dB	
Transmitter and Dispersion Penalty	TDP			2	dB	
Average Power of OFF Transmitter				-30	dBm	
Relative Intensity Noise	RIN			-128	dB/Hz	
Input Differential Impedance	$Z_{IN}$	90	100	110	$\Omega$	
TX Disable	Disable		2.0		$V_{CC}+0.3$	V
	Enable		0		0.8	
TX Fault	Fault		2.0		$V_{CC}+0.3$	V
	Normal		0		0.8	
TX Disable Assert Time		$t_{off}$			10	us
<b>Receiver</b>						
Centre Wavelength	$\lambda_C$	1260		1280	nm	
Sensitivity <sup>*note6</sup>	$P_{IN}$			-15	dBm	
Receiver Overload	$P_{MAX}$	0.5			dBm	
Output Differential Impedance	$P_{IN}$	90	100	110	$\Omega$	
LOS De-Assert	$LOS_D$			-17	dBm	
LOS Assert	$LOS_A$	-29			dBm	
LOS	High		2.0		$V_{CC}+0.3$	V
	Low		0		0.8	

\*Note5. Output is coupled into a 9/125um SMF.

\*Note6: Measured with a PRBS 2<sup>31</sup>-1 test pattern @8.5Gbps.

## Pin Descriptions

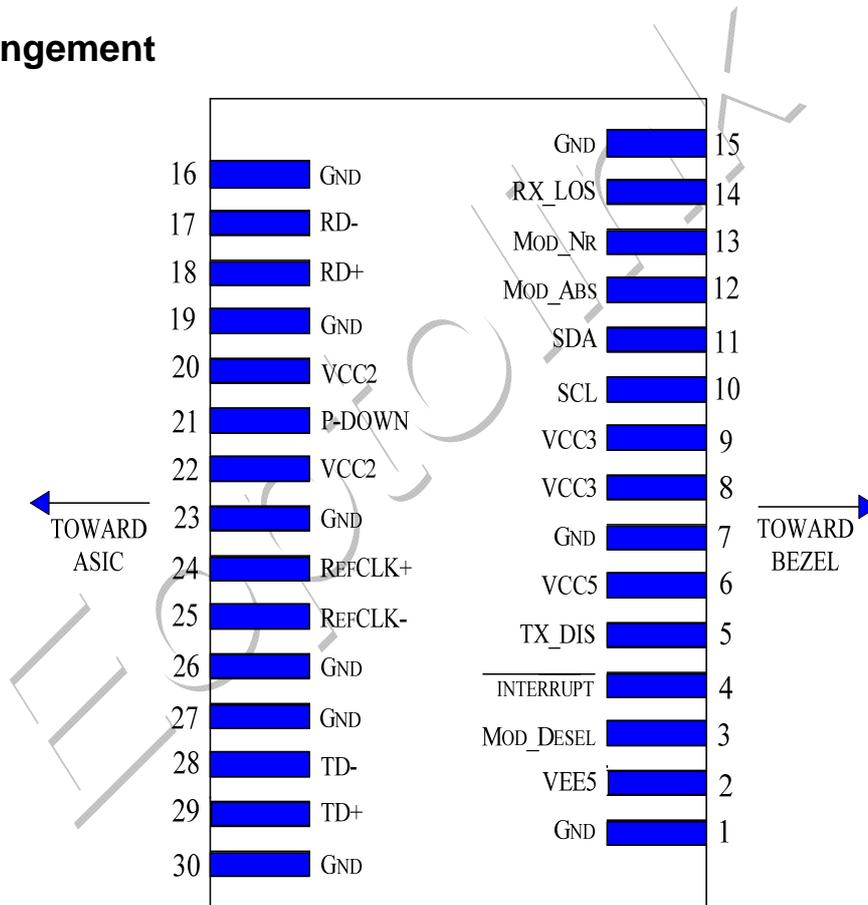
Pin	Logic	Symbol	Name/Description	Ref.
1		GND	Module Ground	1
2		VEE5	Optional –5.2 Power Supply – Not required	
3	LVTTL-I	Mod-Desel	Module De-select; When held low allows the module to , respond to 2-wire serial interface commands	
4	LVTTL-O	Interrupt	Interrupt (bar); Indicates presence of an important condition which can be read over the serial 2-wire interface	2
5	LVTTL-I	TX_DIS	Transmitter Disable; Transmitter laser source turned off	
6		VCC5	+5 Power Supply, Not required	
7		GND	Module Ground	1
8		VCC3	+3.3V Power Supply	
9		VCC3	+3.3V Power Supply	
10	LVTTL-I	SCL	Serial 2-wire interface clock line	2
11	LVTTL-I/O	SDA	Serial 2-wire interface data line	2
12	LVTTL-O	Mod_Abs	Module Absent; Indicates module is not present. Grounded in the module.	2
13	LVTTL-O	Mod_NR	Module Not Ready;	2
14	LVTTL-O	RX_LOS	Receiver Loss of Signal indicator	2
15		GND	Module Ground	1
16		GND	Module Ground	1
17	CML-O	RD-	Receiver inverted data output	
18	CML-O	RD+	Receiver non-inverted data output	
19		GND	Module Ground	1
20		VCC2	+1.8V Power Supply – Not required	
21	LVTTL-I	P_Down/RST	Power Down; When high, places the module in the low power stand-by mode and on the falling edge of P_Down initiates a module reset	
			Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle.	
22		VCC2	+1.8V Power Supply – Not required	
23		GND	Module Ground	1
24	PECL-I	RefCLK+	Reference Clock non-inverted input, AC coupled on the host board – Not required	3
25	PECL-I	RefCLK-	Reference Clock inverted input, AC coupled on the host board – Not required	3
26		GND	Module Ground	1

27		GND	Module Ground	1
28	CML-I	TD-	Transmitter inverted data input	
29	CML-I	TD+	Transmitter non-inverted data input	
30		GND	Module Ground	1

### Notes:

1. Module circuit ground is isolated from module chassis ground within the module.
2. Open collector; should be pulled up with 4.7k – 10k ohms on host board to a voltage between 3.15V and 3.6V.
3. A Reference Clock input is not required.

### Pin Arrangement



**Diagram of Host Board Connector Block Pin Numbers and Name**

### Digital Diagnostic Functions

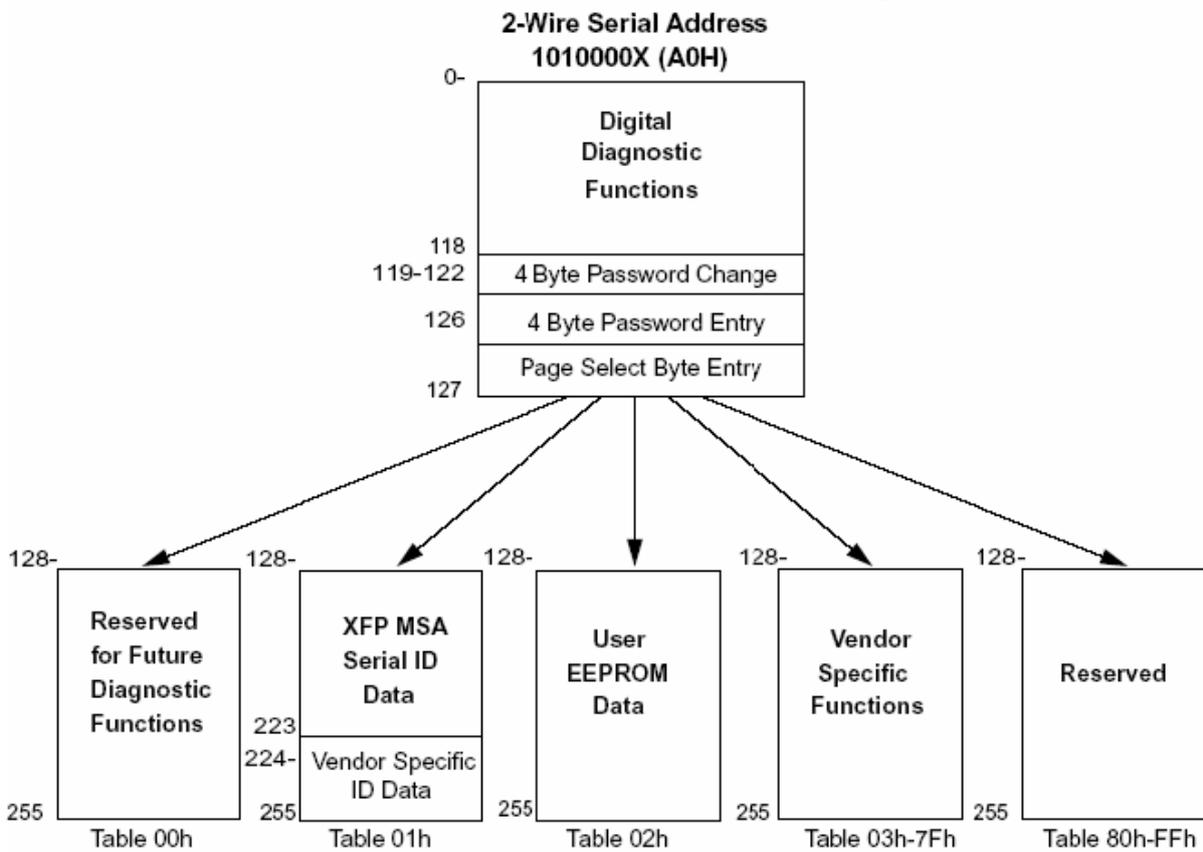
Eoptolink's EOLX-BI1680-16XXL Small Form Factor 10Gb/s (XFP) transceivers are compliant with the current XFP Multi-Source Agreement (MSA) Specification Rev 4.5.

As defined by the XFP MSA, Eoptolink XFP transceivers provide digital diagnostic functions via a 2-wire serial interface, which allows real-time access to the following operating parameters:

- ◆ Transceiver temperature
- ◆ Laser bias current
- ◆ Transmitted optical power

- ◆ Received optical power
- ◆ Transceiver supply voltage

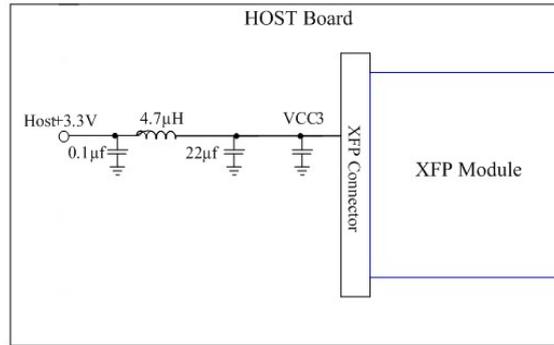
The structure of the memory map is shown in the following figure, which is accessible over a 2-wire serial interface at the 8-bit address 1010000X (A0h). The normal 256 byte I<sup>2</sup>C address space is divided into low and upper blocks of 128 Bytes. The lower block of 128 Bytes is always directly available and is used for the diagnostics and control function. Multiple blocks of memories are available in the upper 128 Bytes of the address space. These are individually addressed through a table select Byte which the user enters into a location in the lower address space. Thus, there is a total available address space of 128\*256 = 32 Kbytes in this upper memory space. The contents of Table 01h are listed in following table. Please refer SFF INF-8077i (Revision 4.5) for detailed information.



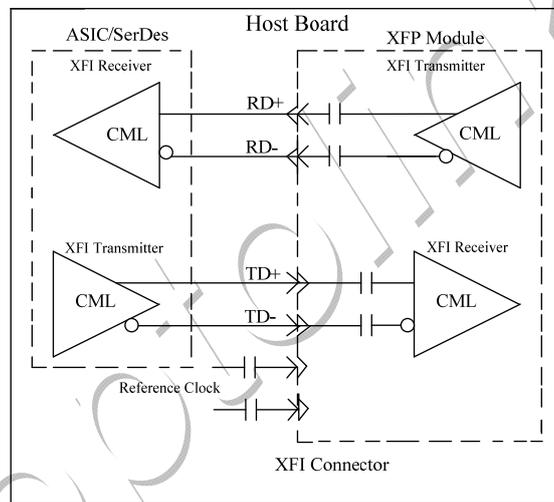
### Monitor Specification:

Data Address	Parameter	Accuracy
96 ~ 97	Temperature	± 3°C
98 ~ 99	Reserved	
100~101	Tx Bias	±10%
102~103	Tx Power	±2dB
104~105	Rx Power	± 2dB
106~107	VCC3	± 3%

## Recommended Host Board Power Supply Circuit

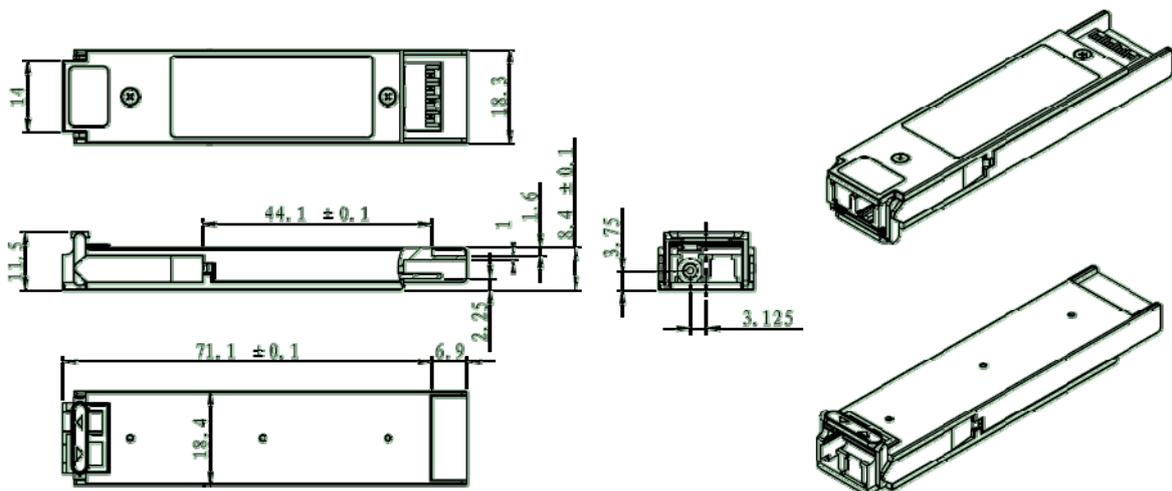


## Recommended High-speed Interface Circuit



## Mechanical Specifications

Eoptolink's XFP transceivers are compliant with the dimensions defined by the XFP Multi-Sourcing Agreement (MSA).



## Eye Safety

This single-mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

## Obtaining Document

You can visit our website: <http://www.eoptolink.com>

Or contact Eoptolink Technology Inc., Ltd. Listed at the end of the documentation to get the latest documents.

## Revision History

Revision	Initiated	Reviewed	Approved	DCN	Release Date
V1.a	Picard	Frank		Released.	July 23, 2014

## Notice:

Eoptolink reserves the right to make changes to or discontinue any optical link product or service identified in this publication, without notice, in order to improve design and/or performance. Applications that are described herein for any of the optical link products are for illustrative purposes only. Eoptolink makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

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