

## EOLS-BI1326-40-DX Series

## EOLS-BI1526-40-DX Series

Single-Mode up to 2.67Gbps  
 Simplex SC/LC Single-Fiber SFP Transceiver  
 RoHS6 Compliant



### Features

- ◆ Operating data rate up to 2.67Gbps
- ◆ 1310/1550nm DFB Laser Transmitter
- ◆ 40km with 9/125um SMF
- ◆ Single 3.3V Power supply and TTL Logic Interface
- ◆ Hot-Pluggable SFP Footprint Simplex SC/LC Connector Interface
- ◆ Class 1 FDA and IEC60825-1 laser safety compliant
- ◆ Operating Case Temperature  
 Standard: 0°C~+70°C  
 Industrial: -40°C~+85°C
- ◆ Compliant with SFP MSA Specification
- ◆ Compliant with SFF-8472

### Applications

- ◆ CPRI/ OBSAI Links
- ◆ Mobile Wireless station
- ◆ Gigabit Ethernet/1x/2x Fiber Channel
- ◆ Other Optical Links

### Ordering information

Part No.	Data Rate	Distance	Interface	Temp.	DDMI
EOLS-BI1326-40-D <sup>*(note1)</sup>	Up to 2.67Gbps	40km	SC	Standard	YES
EOLS-BI1326-40-DI	Up to 2.67Gbps	40km	SC	Industrial	YES
EOLS-BI1326-40-DL	Up to 2.67Gbps	40km	LC	Standard	YES
EOLS-BI1326-40-DIL	Up to 2.67Gbps	40km	LC	Industrial	YES
EOLS-BI1526-40-D <sup>*(note1)</sup>	Up to 2.67Gbps	40km	SC	Standard	YES
EOLS-BI1526-40-DI	Up to 2.67Gbps	40km	SC	Industrial	YES
EOLS-BI1526-40-DL	Up to 2.67Gbps	40km	LC	Standard	YES
EOLS-BI1526-40-DIL	Up to 2.67Gbps	40km	LC	Industrial	YES

Note1: Standard version.

## Regulatory Compliance\*

Product Certificate	Certificate Number	Applicable Standard
TUV	R50135086	EN 60950-1:2006+A11+A1+A12+A2
		EN 60825-1:2014
		EN 60825-2:2004+A1+A2
UL	E317337	UL 60950-1
		CSA C22.2 No. 60950-1-07
EMC CE	AE 50285865 0001	EN 55022:2010
		EN 55024:2010
FCC	WTF14F0514417E	47 CFR PART 15 OCT., 2013
FDA	/	CDRH 1040.10
ROHS	/	2011/65/EU

\*The above certificate number updated to June 2014, because some certificate will be updated every year, such as FDA and ROHS. For the latest certification information, please check with Eoptolink.

## Product Description

The EOLS-BI1X26-40-DX series is small form factor pluggable module for CPRI/OBSAI and Fiber Channel single fiber communications. It is with the SFP 20-pin connector to allow hot plug capability.

The transmitter section uses a distributed feedback laser and is a class 1 laser compliant according to International Safety Standard IEC 60825. The receiver section uses an integrated B type/A type InGaAs Avalanche photo detector preamplifier mounted in an optical header and a limiting post-amplifier IC.

The EOLS-BI1X26-40-DX series are designed to be compliant with SFF-8472 SFP Multi-source Agreement (MSA).

## Absolute Maximum Ratings<sup>\*Note2</sup>

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	$T_s$	-40	+85	°C
Supply Voltage	$V_{CC}$	-0.5	3.6	V
Operating Relative Humidity		-	95	%

Note2: Exceeding any one of these values may destroy the device immediately.

## Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit
Operating Case Temperature	$T_c$	EOLS-BI1X26-40-DX	0	70	°C
		EOLS-BI1X26-40-DIX	-40	+85	
Power Supply Voltage	$V_{CC}$	3.15	3.3	3.45	V
Power Supply Current	$I_{CC}$			300	mA

Date Rate				2.67	Gbps
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**Performance Specifications - Electrical**

Parameter	Symbol	Min.	Typ.	Max	Unit	Notes
<b>Transmitter</b>						
LVPECL Compatible Inputs(Differential)	Vin	400		2000	mVpp	AC coupled inputs <sup>*(note5)</sup>
Input Impedance (Differential)	Zin	85	100	115	ohms	Rin > 100 kohms @ DC
Tx_Dis	Disable	2		Vcc	V	
	Enable	0		0.8		
Tx_FAULT	Fault	2		Vcc+0.3	V	
	normal	0		0.5		
<b>Receiver</b>						
CML Outputs (Differential)	Vout	370		1200	mVpp	AC coupled outputs <sup>*(note5)</sup>
Output Impedance (Differential)	Zout	85	100	115	ohms	
Rx_LOS	LOS	2		Vcc+0.3	V	
	normal	0		0.8	V	
MOD_DEF ( 0:2 )	VoH	2.5			V	With Serial ID
	VoL	0		0.5	V	

**Optical and Electrical Characteristics**
**EOLS-BI1326-40-DX (1310nm DFB and PIN 40km)**

Parameter	Symbol	Min.	Typical	Max.	Unit
9µm Core Diameter SMF	L		40		km
Data Rate				2.67	Gbps
<b>Transmitter</b>					
Center Wavelength	$\lambda_c$	1260	1310	1360	nm
Spectral Width (-20dB)	$\Delta\lambda$			1	nm
Side Mode Suppression Ratio	SMSR	30			dB
Average Output Power <sup>*(note3)</sup>	Pout	1		5	dBm
Extinction Ratio <sup>*(note4)</sup>	ER	8.2			dB
Rise/Fall Time (20%~80%)	tr/tf			150	ps
Output Optical Eye <sup>*(note4)</sup>	IUT-T G.957 Compliant <sup>*(note7)</sup>				
TX_Disable Assert Time	t_off			10	us
Pout@TX Disable Asserted	Pout	-	-	-45	dBm
<b>Receiver</b>					
Center Wavelength	$\lambda_c$	1530		1570	nm
Receiver Sensitivity <sup>*(note6)</sup>	Multirate	Pmin		-18	dBm
Receiver Overload	Pmax	-3			dBm

Optical Path Penalty				1	dB
LOS De-Assert	LOSD			-19	dBm
LOS Assert	LOSA	-35			dBm
LOS Hysteresis <sup>*(note8)</sup>		0.5			dB

## EOLS-BI1526-40-DX (1550nm DFB and PIN 40km)

Parameter	Symbol	Min.	Typical	Max.	Unit	
9µm Core Diameter SMF	L	-	40	-	km	
Data Rate				2.67	Gbps	
<b>Transmitter</b>						
Center Wavelength	$\lambda_C$	1530	1550	1570	nm	
Spectral Width (-20dB)	$\Delta\lambda$	-	-	1	nm	
Side Mode Suppression Ratio	SMSR	30	-	-	dB	
Average Output Power <sup>*(note3)</sup>	P <sub>out</sub>	-5	-	0	dBm	
Extinction Ratio <sup>*(note4)</sup>	ER	8.2	-	-	dB	
Rise/Fall Time (20%~80%)	tr/tf	-	-	150	ps	
Output Optical Eye <sup>*(note4)</sup>	Compliant with ITU-T G.957 <sup>*(note7)</sup>					
TX_Disable Assert Time	t <sub>off</sub>	-	-	10	us	
P <sub>out</sub> @TX Disable Asserted	P <sub>out</sub>	-	-	-45	dBm	
<b>Receiver</b>						
Center Wavelength	$\lambda$	1260	-	1360	nm	
Receiver Sensitivity <sup>*(note6)</sup>	Multirate	P <sub>min</sub>	-	-	-18	dBm
Receiver Overload		P <sub>max</sub>	-3	-	-	dBm
Optical Path Penalty			-	-	1	dB
LOS De-Assert	LOSD	-	-	-19	dBm	
LOS Assert	LOSA	-35	-	-	dBm	
LOS Hysteresis <sup>*(note8)</sup>		0.5	-	-	dB	

Note3: Output is coupled into a 9/125µm single-mode fiber.

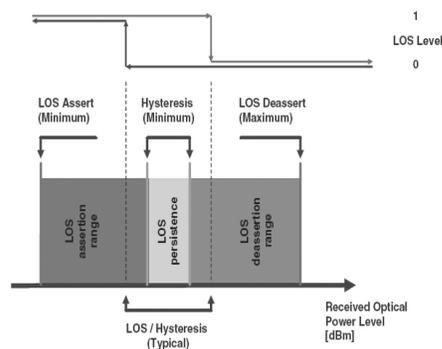
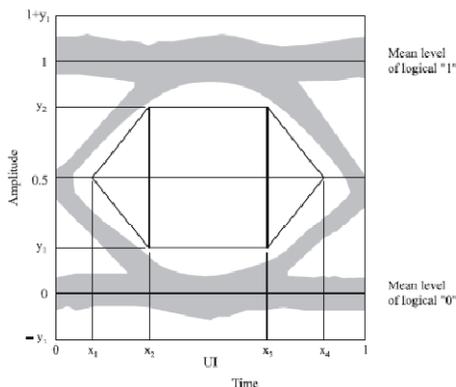
Note4: Filtered, measured with a PRBS 2<sup>23</sup>-1 test pattern @2.5Gbps

Note5: LVPECL/CML logic, internally AC coupled.

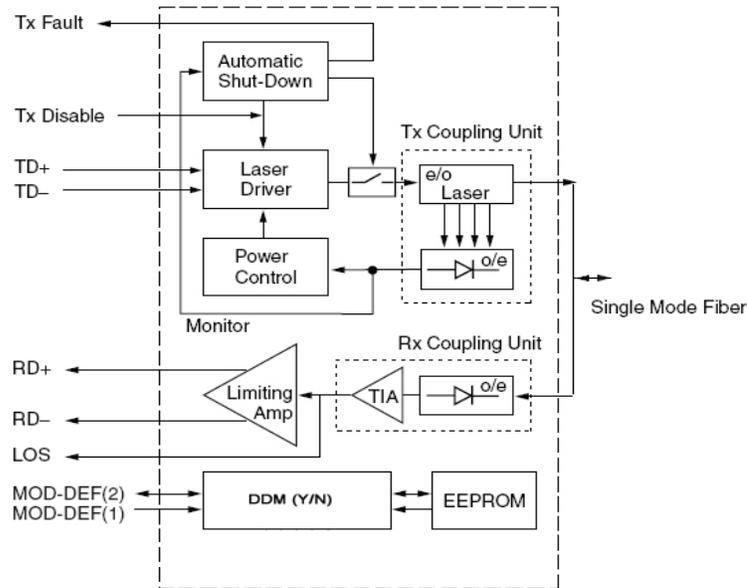
Note6: Measured at 2.5Gbps with ER=9 dB, 2<sup>23</sup>-1 PRBS data pattern, BER <1E-12.

Note7: Eye pattern mask

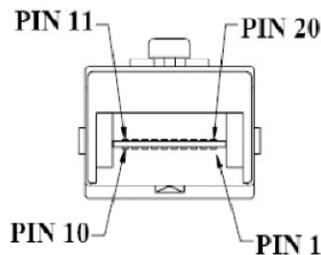
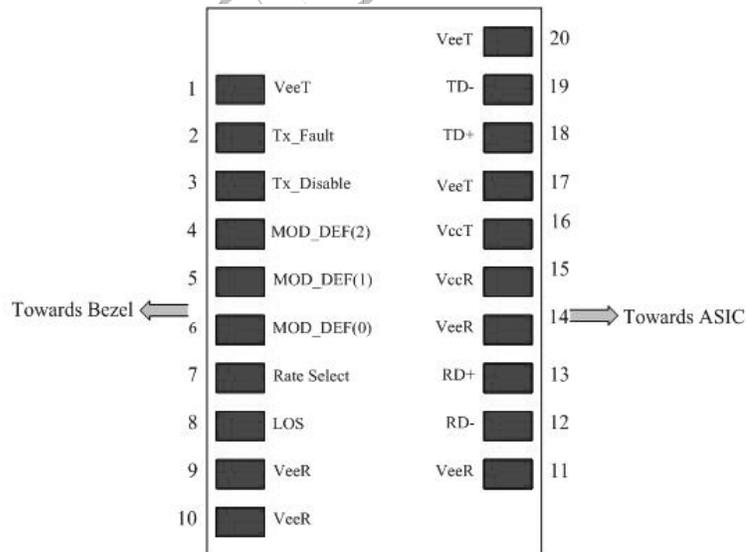
Note8: LOS Hysteresis



### Functional Description of Transceiver



### SFP Transceiver Electrical Pad Layout



## Pin Function Definitions

Pin Num.	Name	FUNCTION	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	5)
2	TX Fault	Transmitter Fault Indication	3	1)
3	TX Disable	Transmitter Disable	3	2), Module disables on high or open
4	MOD-DEF2	Module Definition 2	3	3), Data line for Serial ID.
5	MOD-DEF1	Module Definition 1	3	3), Clock line for Serial ID.
6	MOD-DEF0	Module Definition 0	3	3), Grounded within the module.
7	Rate Select	Not Connect	3	Function not available
8	LOS	Loss of Signal	3	4)
9	VeeR	Receiver Ground	1	5)
10	VeeR	Receiver Ground	1	5)
11	VeeR	Receiver Ground	1	5)
12	RD-	Inv. Received Data Out	3	6)
13	RD+	Received Data Out	3	7)
14	VeeR	Receiver Ground	1	5)
15	VccR	Receiver Power	2	3.3 ± 5%, 7)
16	VccT	Transmitter Power	2	3.3 ± 5%, 7)
17	VeeT	Transmitter Ground	1	5)
18	TD+	Transmit Data In	3	8)
19	TD-	Inv. Transmit Data In	3	8)
20	VeeT	Transmitter Ground	1	5)

### Notes:

1) TX Fault is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

2) TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7~10 K Ω resistor. Its states are:

Low (0 – 0.8V): Transmitter on

(>0.8, < 2.0V): Undefined

High (2.0 – 3.465V): Transmitter Disabled

Open: Transmitter Disabled

3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7K – 10KΩresistor on the host board. The pull-up voltage shall be VccT or VccR (see Section IV for

further details). Mod-Def 0 is grounded by the module to indicate that the module is present  
Mod-Def 1 is the clock line of two wire serial interface for serial ID  
Mod-Def 2 is the data line of two wire serial interface for serial ID

4) LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor. Pull up voltage between 2.0V and  $V_{ccT}$ ,  $R+0.3V$ . When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to  $< 0.8V$ .

5) VeeR and VeeT may be internally connected within the SFP module.

6) RD-/+ : These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 370 and 2000 mV differential (185 – 1000 mV single ended) when properly terminated.

7) VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V  $\pm 5\%$  at the SFP connector pin. Maximum supply current is 300mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 ohm should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30mA greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.

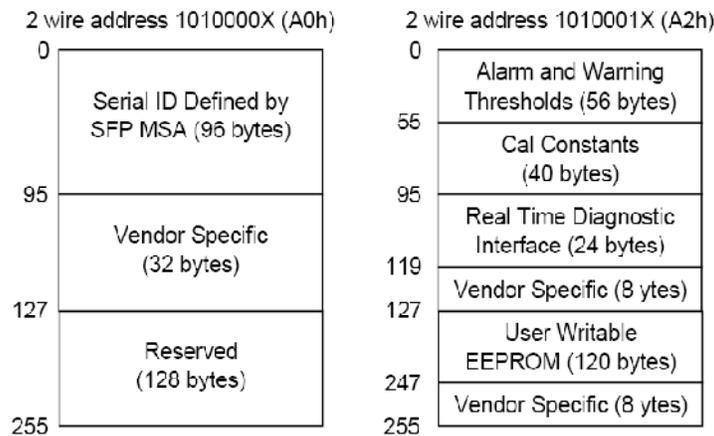
8) TD-/+ : These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 500 – 2400 mV (250 – 1200mV single-ended), though it is recommended that values between 500 and 1200 mV differential (250 – 600mV single-ended) be used for best EMI performance.

## EEPROM

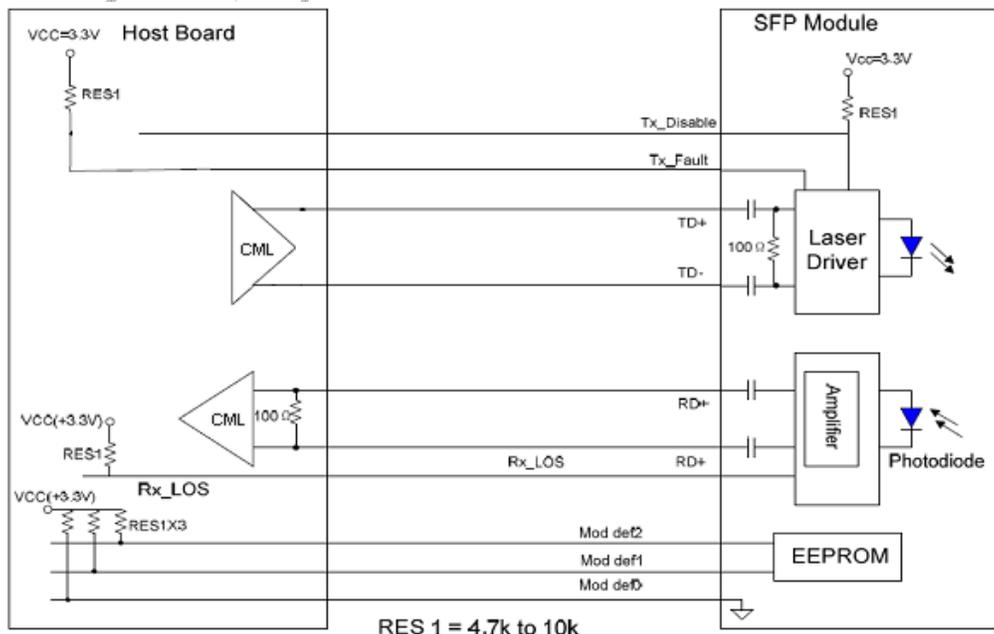
The serial interface uses the 2-wire serial CMOS EEPROM protocol defined for the ATMEL AT24C02/04 family of components. When the serial protocol is activated, the host generates the serial clock signal (SCL). The positive edge clocks data into those segments of the EEPROM that are not writing protected within the SFP transceiver. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories

are organized as a series of 8-bit data words that can be addressed individually or sequentially.

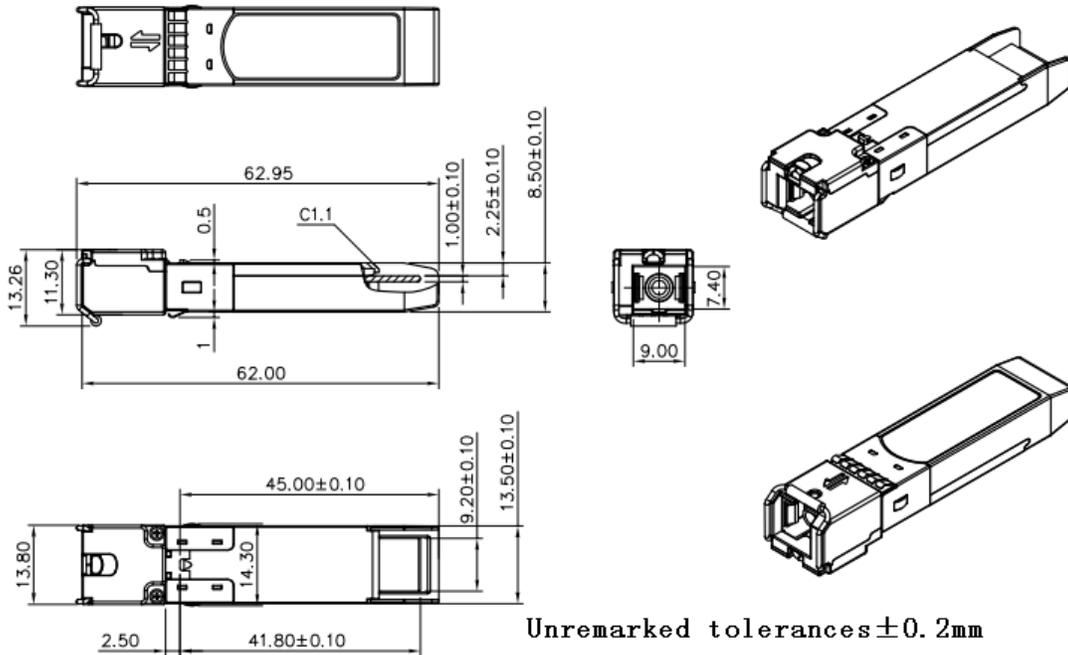
The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. The diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2h. The digital diagnostic memory map specific data field define as following .For detail EEPROM information, please refer to the related document of SFF 8472 Rev 9.3.



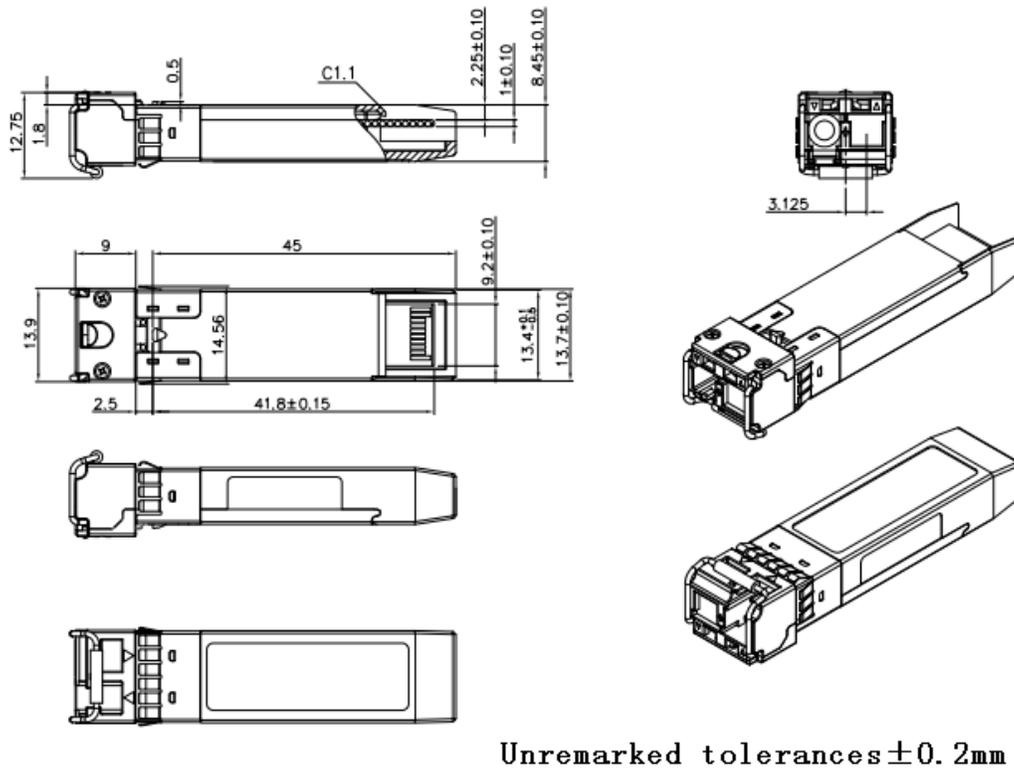
## Recommend Circuit Schematic



Mechanical Specifications

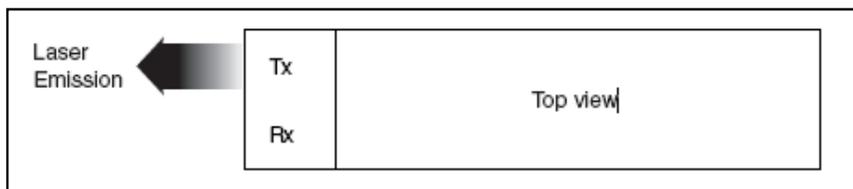


SC



LC

## Laser Emission



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## Revision History

Revision	Initiate	Review	Approve	Subject	Last printed
V1.a	Arvin/Abby	Flagon/Lyn /Kelly		New Released	Feb 4, 2016

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