EOLS-1524-X Series

Single-Mode 1550nm up to 2.5Gbps
Duplex SFP Transceiver
RoHS6 Compliant

Features

◆ Operating Data Rate up to 2.5Gbps

◆ 1550nm DFB Laser Transmitter

◆ 40km with 9/125 μm SMF

    80km with 9/125 μm SMF

    100km with 9/125 μm SMF

    120km with 9/125 μm SMF

◆ Single 3.3V Power Supply and TTL Logic Interface

◆ Hot-Pluggable SFP Footprint Duplex LC Connector

    Interface

◆ Class 1 FDA and IEC60825-1 Laser Safety Compliant

◆ Operating Case Temperature

    Standard: 0℃~70℃

    Industrial:-40℃~+85℃

◆ Compliant with SFP MSA Specification

◆ Compliant with SFF-8472

Applications

◆ STM16 Optical Interface

◆ Fiber Channel Switch Infrastructure

◆ ATM Switches and Routers

◆ SONET / SDH Switch Infrastructure

◆ Other Optical Links

Ordering Information

Part No.	Data Rate	Fiber	Distance	Interface	Temp.	DDMI
EOLS-1524-40*(note1)	2.5Gbps	SMF	40km	LC	Standard	NO
EOLS-1524-40-I	2.5Gbps	SMF	40km	LC	Industrial	NO
EOLS-1524-40-D	2.5Gbps	SMF	40km	LC	Standard	YES
EOLS1-524-40-DI	2.5Gbps	SMF	40km	LC	Industrial	YES
EOLS-1524-80*(note1)	2.5Gbps	SMF	80km	LC	Standard	NO
EOLS-1524-80-I	2.5Gbps	SMF	80km	LC	Industrial	NO
EOLS-1524-80-D	2.5Gbps	SMF	80km	LC	Standard	YES
EOLS-1524-80-DI	2.5Gbps	SMF	80km	LC	Industrial	YES
EOLS-1524-A*(note1)	2.5Gbps	SMF	100km	LC	Standard	NO

EOLS-1524-A-I	2.5Gbps	SMF	100km	LC	Industrial	NO
EOLS-1524-A-D	2.5Gbps	SMF	100km	LC	Standard	YES
EOLS-1524-A-DI	2.5Gbps	SMF	100km	LC	Industrial	YES
EOLS-1524-B*(note1)	2.5Gbps	SMF	120km	LC	Standard	NO
EOLS-1524-B-I	2.5Gbps	SMF	120km	LC	Industrial	NO
EOLS-1524-B-D	2.5Gbps	SMF	120km	LC	Standard	YES
EOLS-1524-B-DI	2.5Gbps	SMF	120km	LC	Industrial	YES

Note1: Standard version

*The product image only for reference purpose.

Regulatory Compliance*Note2

Product Certificate	Certificate Number	Applicable Standard
TUV	R50135086	EN 60950-1:2006+A11+A1+A12+A2
		EN 60825-1:2014
		EN 60825-2:2004+A1+A2
UL	E317337	UL 60950-1
		CSA C22.2 No. 60950-1-07
EMC CE	AE 50285865 0001	EN 55022:2010
		EN 55024:2010
FCC	WTF14F0514417E	47 CFR PART 15 OCT., 2013
FDA	/	CDRH 1040.10
ROHS	/	2011/65/EU

Note2: The above certificate number updated to June 2014, because some certificate will be updated every year, such as FDA and ROHS. For the latest certification information, please check with Eoptolink.

Product Description

The EOLS-1524-X series single mode transceiver is small form factor pluggable module for duplex serial optical data communication application such as SONET OC-48 / SDH STM-16, Gigabit Ethernet 1000BASE-LX and Fiber Channel 1x/2x SM-LC-L FC-PI. It is with the SFP 20-pin connector to allow hot plug capability. This module is designed for single mode fiber and operates at a nominal wavelength of 1550nm.

The transmitter section uses a multiple quantum well 1550nm DFB laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an integrated InGaAs Avalanche photo detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

The EOLS-1524-X-D series are designed to be compliant with SFF-8472 Multi-Source Agreement (MSA).

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	T_S	-40	+85	°C
Supply Voltage	V_{CC}	-0.5	3.6	V
Operating Relative Humidity		-	95	%

*Exceeding any one of these values may destroy the device permanently.

Recommended Operating Conditions

Parameter		Symbol		Min.	Typical	Max.	Unit
Operating Case Temperature		T_c	EOLS-1524-X	0	-	+70	°C
			EOLS-1524-X-I	-40	-	+85	
Power Supply Voltage		V_{CC}		3.15	3.3	3.45	V
Power Supply Current		I_{CC}		-	-	300	mA
Date Rate	OC-48/STM-16			-	2.488	-	Gbps
	2FC			-	2.125	-	

Performance Specifications - Electrical

Parameter		Symbol	Min.	Typ.	Max	Unit	Notes
Transmitter							
CML Inputs(Differential)		Vin	400	-	1600	mVpp	AC coupled inputs*(note3)
Input Impedance (Differential)		Zin	85	100	115	ohm	Rin > 100 kohm @ DC
TX_Dis	Disable		2	-	Vcc	V	
	Enable		0	-	0.8		
TX_FAULT	Fault		2	-	Vcc+0.3	V	
	Normal		0	-	0.5		
Receiver							
CML Outputs (Differential)		Vout	400	800	1200	mVpp	AC coupled outputs*(note4)
Output Impedance (Differential)		Zout	85	100	115	ohm	
RX_LOS	LOS		2	-	Vcc+0.3	V	
	Normal		0	-	0.8	V	
MOD_DEF (0:2)	VoH		2.5	-	-	V	With Serial ID
	VoL		0	-	0.5	V	

Performance Specifications - Optical

(1550nm DFB and PIN, 40km)

Parameter		Symbol	Min.	Typical	Max.	Unit
9µm Core Diameter SMF		L	-	40	-	km
Data Rate			1	2.488		Gbps
Transmitter						
Center Wavelength		λ$_C$	1500	1550	1600	nm
Spectral Width (-20dB)		Δλ	-	-	1	nm
Side Mode Suppression Ratio		SMSR	30	-	-	dB
Average Output Power*[note5]		Pout	-2	-	+3	dBm
Extinction Ratio*[note6]		ER	8.2	-	-	dB
Rise/Fall Time(20%～80%)		tr/tf	-	-	150	ps
Output Optical Eye*[note6]		Compliant with ITU-T G.957*[note8]				
TX_Disable Assert Time		t_off	-	-	10	us
P$_{out}$@TX Disable Asserted		Pout	-	-	-45	dBm
Receiver						
Center Wavelength		λ	1260	-	1600	nm
Receiver Sensitivity*[note7]	OC-48/STM-16	Pmin	-	-	-20	dBm
	2xFC		-	-	-20	
Receiver Overload		Pmax	-3	-	-	dBm
Optical Path Penalty			-	-	1	dB
Reflection			-	-	-27	dB
LOS De-Assert		LOSD	-	-	-21	dBm
LOS Assert		LOSA	-35	-	-	dBm
LOS Hysteresis*[note9]			0.5	-	-	dB

(1550nm DFB and APD, 80km)

Parameter	Symbol	Min.	Typical	Max.	Unit
9µm Core Diameter SMF	L	-	80	-	km
Data Rate		1	2.488		Gbps
Transmitter					
Center Wavelength	λ$_C$	1500	1550	1600	nm
Spectral Width (-20dB)	Δλ	-	-	1	nm
Side Mode Suppression Ratio	SMSR	30	-	-	dB
Average Output Power*[note5]	Pout	-2	-	+3	dBm
Extinction Ratio*[note6]	ER	8.2	-	-	dB
Rise/Fall Time(20%～80%)	tr/tf	-	-	150	ps
Output Optical Eye*[note6]	Compliant with ITU-T G.957*[note8]				
TX_Disable Assert Time	t_off	-	-	10	us
P$_{out}$@TX Disable Asserted	Pout	-	-	-45	dBm
Receiver					

Center Wavelength		λ	1260	-	1600	nm
Receiver Sensitivity*[note7]	OC-48/STM-16	Pmin	-	-	-28	dBm
	2xFC		-	-	-28	
Receiver Overload		Pmax	-9	-	-	dBm
Optical Path Penalty			-	-	2	dB
Reflection			-	-	-27	dB
LOS De-Assert		LOSD	-	-	-29	dBm
LOS Assert		LOSA	-42	-	-	dBm
LOS Hysteresis*[note9]			0.5	-	-	dB

(1550nm DFB and APD, 100km)

Parameter	Symbol	Min.	Typical	Max.	Unit
9µm Core Diameter SMF	L	-	100	-	km
Data Rate		1	2.488		Gbps
Transmitter					
Center Wavelength	λ_C	1500	1550	1600	nm
Spectral Width (-20dB)	Δλ	-	-	1	nm
Side Mode Suppression Ratio	SMSR	30	-	-	dB
Average Output Power*[note5]	Pout	0	-	+5	dBm
Extinction Ratio*[note6]	ER	8.2	-	-	dB
Rise/Fall Time(20%～80%)	tr/tf	-	-	150	ps
Output Optical Eye*[note6]	Compliant with ITU-T G.957*[note8]				
TX_Disable Assert Time	t_off	-	-	10	us
Pout@TX Disable Asserted	Pout	-	-	-45	dBm
Receiver					
Center Wavelength	λ	1260	-	1600	nm
Receiver Sensitivity*[note7] OC-48/STM-16	Pmin	-	-	-29	dBm
2xFC		-	-	-29	
Receiver Overload	Pmax	-9	-	-	dBm
Optical Path Penalty		-	-	3	dB
Reflection		-	-	-27	dB
LOS De-Assert	LOSD	-	-	-30	dBm
LOS Assert	LOSA	-42	-	-	dBm
LOS Hysteresis*[note9]		0.5	-	-	dB

(1550nm DFB and APD, 120km)

Parameter	Symbol	Min.	Typical	Max.	Unit
9µm Core Diameter SMF	L	-	120	-	km
Data Rate		1	2.488		Gbps
Transmitter					
Center Wavelength	λ_C	1500	1550	1600	nm
Spectral Width (-20dB)	Δλ	-	-	1	nm
Side Mode Suppression Ratio	SMSR	30	-	-	dB
Average Output Power*[note5]	Pout	2	-	7	dBm

Extinction Ratio*[(note6)]	ER	8.2	-	-	dB
Rise/Fall Time(20%～80%)	tr/tf	-	-	150	ps
Output Optical Eye*[(note6)]	Compliant with ITU-T G.957*[(note8)]				
TX_Disable Assert Time	t_off	-	-	10	us
P_{out}@TX Disable Asserted	Pout	-	-	-45	dBm
Receiver					
Center Wavelength	λ	1260	-	1600	nm
Receiver Sensitivity*[(note7)] OC-48/STM-16	Pmin	-	-	-30	dBm
Receiver Sensitivity*[(note7)] 2xFC	Pmin	-	-	-30	dBm
Receiver Overload	Pmax	-10	-	-	dBm
Optical Path Penalty		-	-	3	dB
Reflection		-	-	-27	dB
LOS De-Assert	LOSD	-	-	-31	dBm
LOS Assert	LOSA	-45	-	-	dBm
LOS Hysteresis*[(note9)]		0.5	-	-	dB

Note3: LVPECL logic, internally AC coupled and terminated to 100 differential loads.

Note4: CML logic, internally AC coupled.

Note5: Output is coupled into a 9/125μm single-mode fiber.

Note6: Filtered, measured with a PRBS 2^{23}-1 test pattern @2.488Gbps

Note7: Minimum average optical power measured at BER less than 1E-12, with a 2^{23}-1 PRBS and ER=9dB.
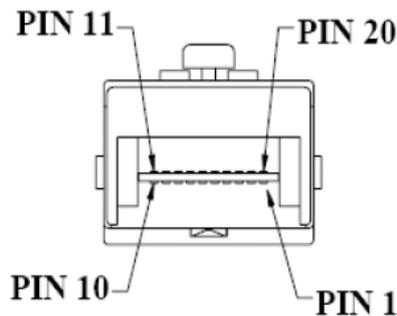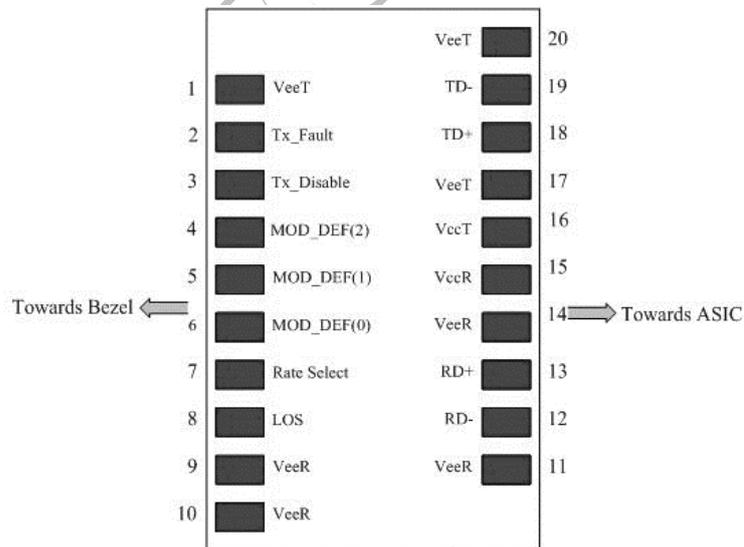
Note8: Eye Pattern Mask
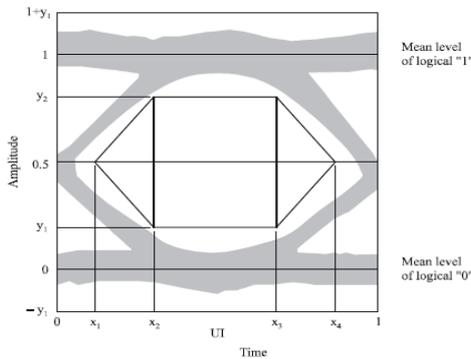
Note9: LOS Hysteresis
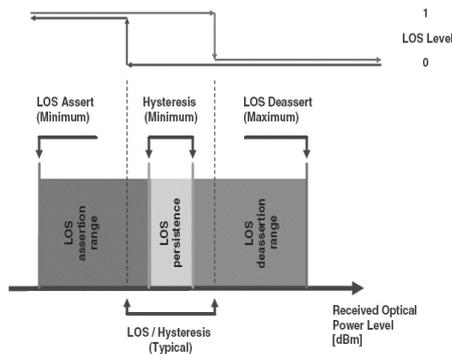
Functional Description of Transceiver
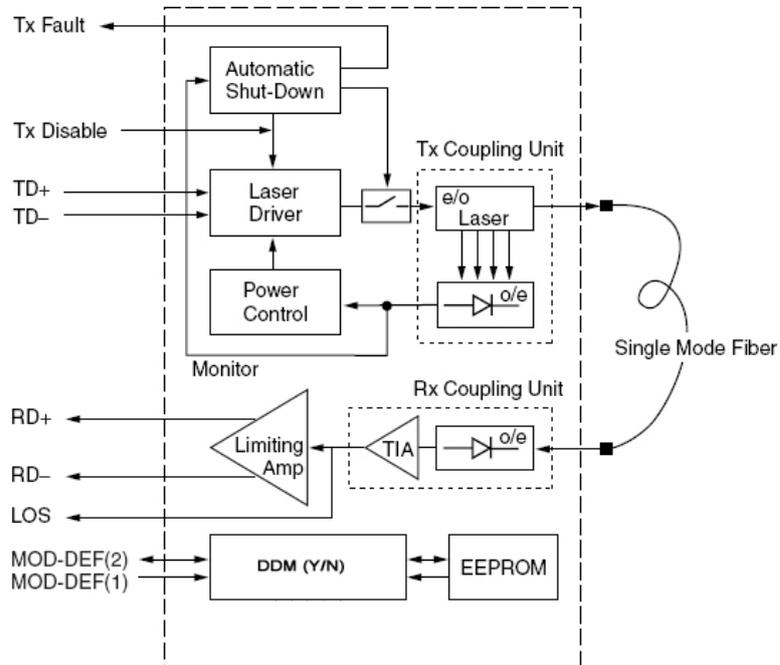
SFP Transceiver Electrical Pad Layout

Pin Function Definitions

Pin Num.	Name	FUNCTION	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	5)
2	TX Fault	Transmitter Fault Indication	3	1)
3	TX Disable	Transmitter Disable	3	2) Module disables on high or open
4	MOD-DEF2	Module Definition 2	3	3) Data line for Serial ID.
5	MOD-DEF1	Module Definition 1	3	3) Clock line for Serial ID.
6	MOD-DEF0	Module Definition 0	3	3) Grounded within the module.
7	Rate Select	Not Connect	3	Function not available
8	LOS	Loss of Signal	3	4)
9	VeeR	Receiver Ground	1	5)
10	VeeR	Receiver Ground	1	5)
11	VeeR	Receiver Ground	1	5)
12	RD-	Inv. Received Data Out	3	6)
13	RD+	Received Data Out	3	6)
14	VeeR	Receiver Ground	1	5)
15	VccR	Receiver Power	2	7) $3.3 \pm 5\%$
16	VccT	Transmitter Power	2	7) $3.3 \pm 5\%$
17	VeeT	Transmitter Ground	1	5)
18	TD+	Transmit Data In	3	8)
19	TD-	Inv. Transmit Data In	3	8)
20	VeeT	Transmitter Ground	1	5)

Notes:

1) TX Fault is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

2) TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7 – 10 KΩ resistor. Its states are:

Low (0 – 0.8V): Transmitter on
(>0.8, < 2.0V): Undefined
High (2.0 – 3.465V): Transmitter Disabled
Open: Transmitter Disabled

) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7K – 10K   resistor on the host board. The pull-up voltage shall be VccT or VccR .

Mod-Def 0 is grounded by the module to indicate that the module is present

Mod-Def 1 is the clock line of two wire serial interface for serial ID

Mod-Def 2 is the data line of two wire serial interface for serial ID

4) LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

5) VeeR and VeeT may be internally connected within the SFP module.

6) RD-/+: These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board.

7) VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V ±5% at the SFP connector pin. Maximum supply current is 300mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 ohm should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30mA greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.

8) TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board.

EEPROM

The serial interface uses the 2-wire serial CMOS EEPROM protocol defined for the ATMEL AT24C02/04 family of components. When the serial protocol is activated, the host generates the serial clock signal (SCL). The positive edge clocks data into those segments of the EEPROM that are not writing protected within the SFP transceiver. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. If the module is defined as external calibrated, the diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2H. The digital diagnostic memory map specific data field define as following .For detail EEPROM information,

please refer to the related document of SFF 8472 Rev 9.3.
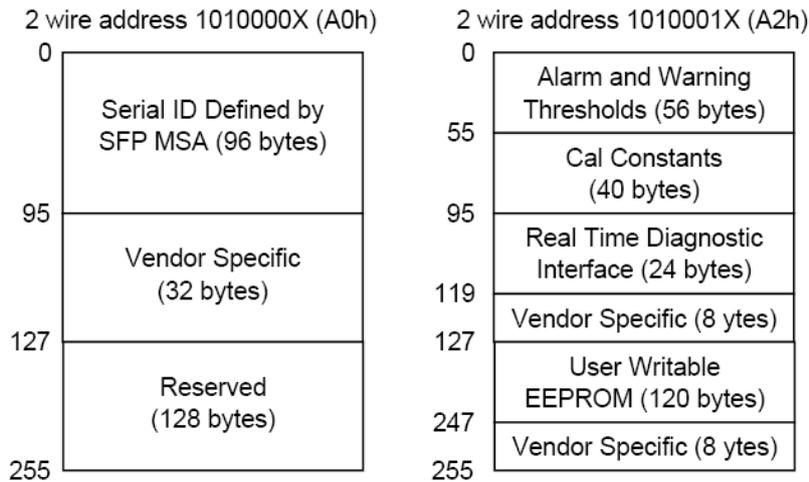
EEPROM Serial ID Memory Contents

Accessing Serial ID Memory uses the 2 wire address 1010000X (A0H). Memory Contents of Serial ID are shown in Table 1.

Table 1 Serial ID Memory Contents

Addr.	Size (Bytes)	Name of Field	Hex	Description
BASE ID FIELDS				
0	1	Identifier	03	SFP
1	1	Ext. Identifier	04	SFP function is defined by serial ID only
2	1	Connector	07	LC Connector
3-10	8	Transceiver	XX[(note9)]	Transmitter Code
11	1	Encoding	03	NRZ
12	1	BR, Nominal	19	2.5Gbps
13	1	Reserved	00	
14	1	Length (9μm)km	28/50/64/78	Transceiver transmit distance
15	1	Length(9μm)100m	FF	
16	1	Length (50μm) 10m	00	
17	1	Length(62.5μm)10m	00	
18	1	Length (Copper)	00	Not compliant
19	1	Reserved	00	
20-35	16	Vendor name	XX XX XX XX XX XX XX XX[(note9)]20 20 20 20 20 20 20 20	Vendor name (ASCII)
36	1	Reserved	00	
37-39	3	Vendor OUI	XX XX XX[(note10)]	
40-55	16	Vendor PN	XX XX XX XX XX XX XX XX XX XX XX XX	Part number

			XX XX XX XX[note10]	
56-59	4	Vendor rev	XX XX XX XX[note10]	
60-61	2	Wavelength	06 0E	1550nm
62	1	Reserved	00	
63	1	CC_BASE	Check Sum (Variable)	Check code for Base ID Fields
EXTENDED ID FIELDS				
64-65	2	Options	00 1A	TX_DISABLE, TX_FAULT and Loss of Signal implemented.
66	1	BR, max	00	
67	1	BR, min	00	
68-83	16	Vendor SN	XX XX XX XX XX XX XX XX 20 20 20 20 20 20 20 20[note10]	Serial Number of transceiver (ASCII). For example "B000822".
84-91	8	Date code	XX XX XX XX XX XX XX XX[note10]	Manufactory date code. For example "080405".
92	1	Diagnostic Monitoring Type	XX[note10]	Digital diagnostic monitoring implemented
93	1	Enhanced Options	XX[note10]	Optional flags
94	1	SFF_8472 Compliance	XX[note10]	01 for diagnostics (Rev9.3 SFF-8472).
95	1	CC_EXT	Check Sum (Variable)	Check sum for Extended ID Field.
VENDOR SPECIFIC ID FIELDS				
96-127	32	Vendor Specific	Read only	Depends on customer information
128-255	128	Reserved	Read only	

Note10: The "XX" byte should be filled in according to practical case. For more information, please refer to the related document of SFP Multi-Source Agreement (MSA).
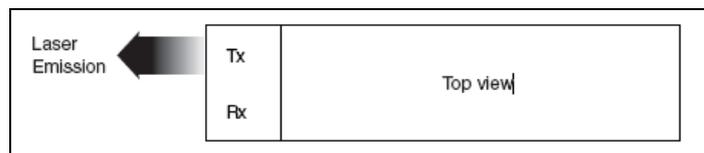
Recommended Circuit Schematic
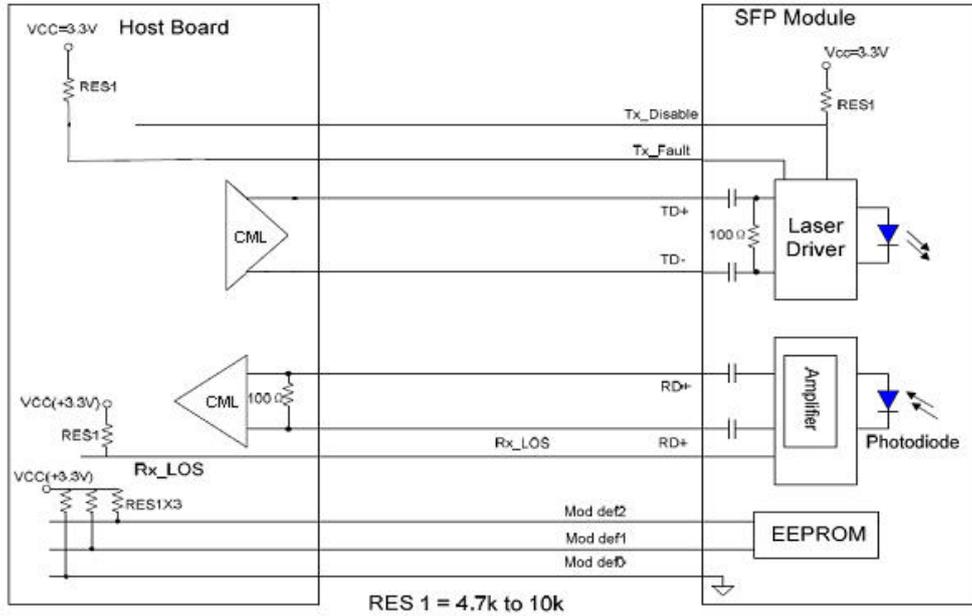
Mechanical Specifications

Unremarked tolerances±0.2mm
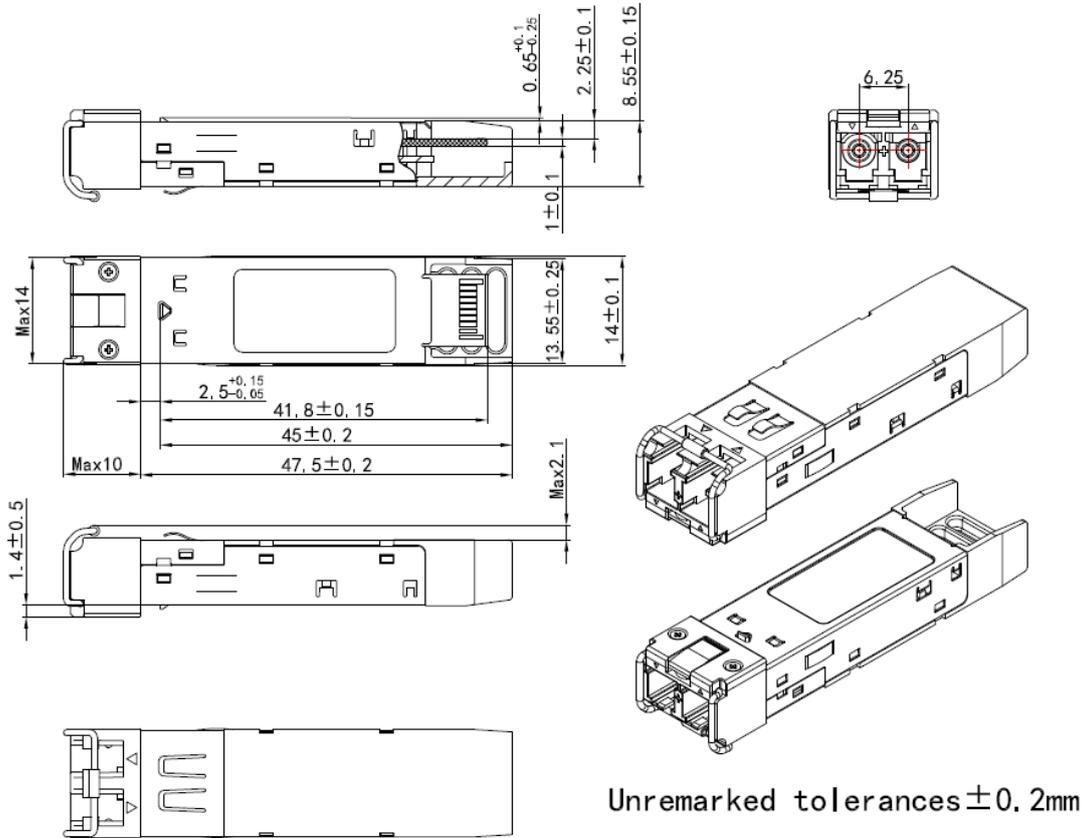
*This 2D drawing only for reference, please check with Eoptolink before ordering.

Laser Emission

Obtaining Document

You can visit our website:

http://www.eoptolink.com

Or contact Eoptolink Technology Inc., Ltd. listed at the end of the documentation to get the latest documents.

Revision History

Revision	Initiated	Reviewed	Approved	Revision History	Release Date
V3.a	Cathy	Kelly		Released.	Feb 23, 2010
V3.b	Cathy			Update PN and mechanical spec.	November 24, 2010
V4.a	Jans			Update LOGO.	August 10, 2011
V4.b	Jans	Kelly		Integrate 4 products, update photo.	Sep 22, 2011
V4.c	Angela	Lyn/Jason /Walt/Nygai	Phlio	Update the regulatory compliance, LOSA and pin definition note3.	Sep 11,2014
V4.d	Angela	Kelly		Update Optical Path Penalty of the 80km/100km/120km.	Sep 12,2014
V4.e	Angela	Kelly/Vina/ Dean		Update the regulatory compliance and 2d drawing.	May 20,2016
V4.f	Angela	Kelly/William/ Chao.Wang		Updated the product image and 2D drawing.	April 17, 2018

Notice:

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