

EOLP-BI1660-9AD & EOLP-BI1660-9DA Series

Tx: 1270nm/Rx: 1330nm BIDI SFP+ Transceiver for CPRI&OBSAI

Tx: 1330nm/Rx: 1270nm BIDI SFP+ Transceiver for CPRI&OBSAI

Multi Rate 600Mbps~6.25Gbps

RoHS 6 Compliant

Features

- ◆ Operating data rate up to 6.25Gbps
- ◆ Two types:
 - A: 1270nm DFB Transmitter/ 1330nm Receiver
 - B: 1330nm DFB Transmitter/ 1270nm Receiver
- ◆ Power budget of 9dB
- ◆ Single 3.3V Power supply and TTL Logic Interface
- ◆ SC Connector Interface
- ◆ Hot Pluggable
- ◆ Power Dissipation < 1.2W
- ◆ Operating Case Temperature
 - Standard: 0~+70°C
 - Extended: -10~+85°C
- ◆ Compliant with SFP+ MSA Specification SFF-8431
- ◆ Compliant with SFF-8472



Applications

- ◆ High speed storage area networks
- ◆ OBSAI interface, such as
6.144/1.536/3.072/1.536Gbps and
768Mbps
- ◆ CPRI interface, such as
6.144/3.072/2.4576/1.228Gbps and
614Mbps

Ordering information

Part No.	Data Rate	Laser	Temp.	Power budget	Optical Interface	DDMI
EOLP-BI1660-9AD*	0.6~6.25Gbps	1270nm DFB	Standard	9dB	SC	YES
EOLP-BI1660-9DA	0.6~6.25Gbps	1330nm DFB	Standard	9dB	SC	YES
EOLP-BI1660-9ADI	0.6~6.25Gbps	1270nm DFB	Extended	9dB	SC	YES
EOLP-BI1660-9DAI	0.6~6.25Gbps	1330nm DFB	Extended	9dB	SC	YES

*Note1: Standard version.

Regulatory Compliance

Product Certificate	Certificate Number	Applicable Standard
TUV	R50135086	EN 60950-1:2006+A11+A1+A12
		EN 60825-1:2007
		EN 60825-2:2004+A1+A2
UL	E317337	UL 60950-1
		CSA C22.2 No. 60950-1-07
EMC CE	AE 50135430 0001	EN 55022:2006
		EN 55024:1998+A1+A2
CB	JPTUV-024038-M1	IEC 60825-2
		IEC 60950-1
FCC	WTF13F0503735E	47 CFR PART 15 OCT., 2010
	WTF13F0503732E	47 CFR PART 15 OCT., 2010
FDA	1230816-000	CDRH 1040.10
ROHS	RLSZF00163462	2011/65/EU

Product Description

The EOLP-BI1660-9XX series single mode transceiver is small form factor pluggable module for bidirectional optical data communications, such as OBSAI and CPRI optical links. It is with the SFP+ 20-pin connector to allow hot plug capability.

The EOLP-BI1660-9AD module is designed for single mode fiber and operates at a nominal wavelength of 1270nm; EOLP-BI1660-9DA module is designed for single mode fiber and operates at a nominal wavelength of 1330nm. The transmitter section uses a multiple quantum well DFB, which is class 1 laser compliant according to International Safety Standard IEC-60825.

The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

Absolute Maximum Ratings*

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	T_S	-40	+85	°C
Operating Case Temperature	$T_C@EOLP-BI1660-9AD$	0	70	°C
	$T_C@EOLP-BI1660-9ADI$	-10	85	°C
Supply Voltage	V_{CC}	-0.5	3.6	V

*Note2: Exceeding any one of these values may destroy the device permanently.

Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit
Power Supply Voltage	V_{CC}	3.14	3.3	3.46	V
Power Supply Current	I_{CC}			365	mA
Surge Current	I_{Surge}			+30	mA
Operating Case Temperature	$T_C@EOLP-BI1660-9AD$	0		70	°C
	$T_C@EOLP-BI1660-9ADI$	-10		85	°C
Baud Rate		0.6		6.25	GBaud

Performance Specifications - Electrical

Parameter	Symbol	Min.	Typ.	Max	Unit	Notes
Transmitter						
CML Inputs(Differential)	V_{in}	150		1200	mVpp	AC coupled inputs
Input Impedance (Differential)	Z_{in}	85	100	115	ohms	$R_{in} > 100 \text{ kohms}$ @ DC
Tx_DISABLE Input Voltage - High		2		$V_{CC}+0.3$	V	
Tx_DISABLE Input Voltage - Low		0		0.8	V	
Tx_FAULT Output Voltage - High		2		$V_{CC}+0.3$	V	$I_o = 400\mu A$; Host V_{CC}
Tx_FAULT Output Voltage - Low		0		0.5	V	$I_o = -4.0mA$
Receiver						
CML Outputs (Differential)	V_{out}	350		700	mVpp	AC coupled outputs
Output Impedance (Differential)	Z_{out}	85	100	115	ohms	
Rx_LOS Output Voltage - High		2		$V_{CC}+0.3$	V	$I_o = 400\mu A$; Host V_{CC}
Rx_LOS Output		0		0.8	V	$I_o = -4.0mA$

Voltage - Low						
MOD_DEF (2:0)	VoH	2.5			V	With Serial ID
	VoL	0		0.5	V	

Optical and Electrical Characteristics

(EOLP-BI1660-9AD, 1270nm DFB & PIN/TIA)

Parameter	Symbol	Min.	Typical	Max.	Unit
Power Budget		9			dB
Data Rate		0.6		6.25	Gbps
Transmitter					
Centre Wavelength	λ_C	1260	1270	1280	nm
Spectral Width (-20dB)	$\Delta\lambda$			1	nm
Average Output Power ^{*note3}	$P_{out, AVG}$	-5		0	dBm
Extinction Ratio	ER	4.5	6		dB
Side Mode Suppression Ratio	SMSR	30			dB
Transmitter and Dispersion Penalty	TDP			2	dB
Average Power of OFF Transmitter				-30	dBm
Relative Intensity Noise	RIN			-128	dB/Hz
Input Differential Impedance	Z_{IN}	90	100	110	Ω
TX Disable	Disable	2.0		$V_{CC}+0.3$	V
	Enable	0		0.8	
TX Fault	Fault	2.0		$V_{CC}+0.3$	V
	Normal	0		0.8	
TX Disable Assert Time	t_{off}			10	us
Receiver					
Centre Wavelength	λ_C	1320		1340	nm
Sensitivity ^{*note4}	P_{IN}			-14	dBm
Receiver Overload	P_{MAX}	0.5			dBm
Output Differential Impedance	P_{IN}	90	100	110	Ω
LOS De-Assert	LOS_D			-18	dBm
LOS Assert	LOS_A	-30			dBm
LOS	High	2.0		$V_{CC}+0.3$	V
	Low	0		0.8	

(EOLP-BI1660-9DA, 1330nm DFB & PIN/TIA)

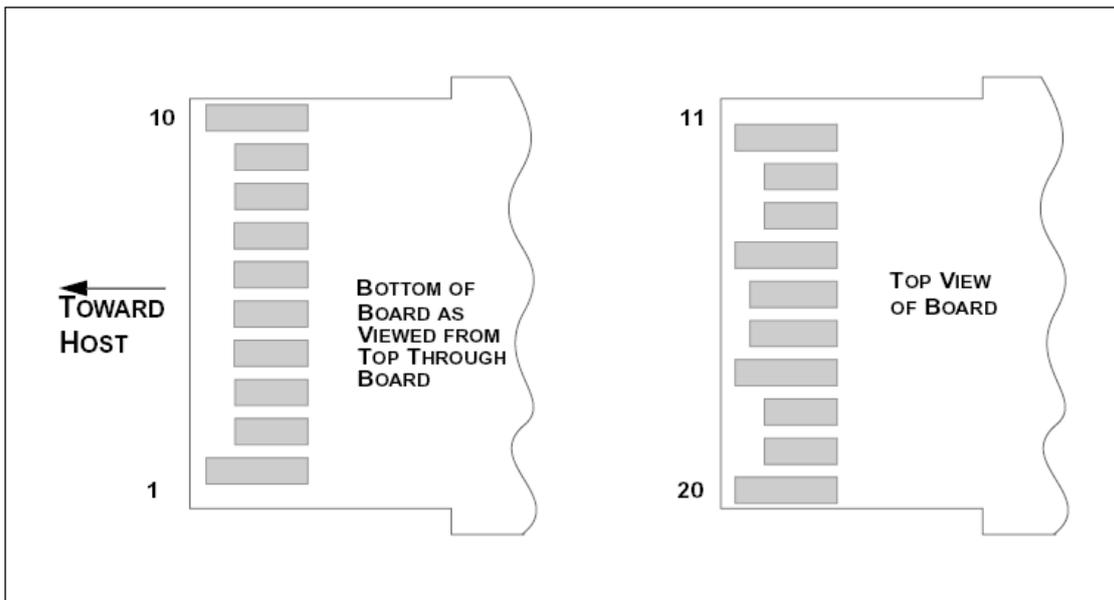
Parameter	Symbol	Min.	Typical	Max.	Unit
Power Budget		9			dB
Data Rate		0.6		6.25	Gbps
Transmitter					
Centre Wavelength	λ_C	1320	1330	1340	nm
Spectral Width (-20dB)	$\Delta\lambda$			1	nm
Average Output Power ^{*note3}	$P_{out, AVG}$	-5		0	dBm
Extinction Ratio	ER	4.5	6		dB

Side Mode Suppression Ratio		SMSR	30			dB
Transmitter and Dispersion Penalty		TDP			2	dB
Average Power of OFF Transmitter					-30	dBm
Relative Intensity Noise		RIN			-128	dB/Hz
Input Differential Impedance		Z _{IN}	90	100	110	Ω
TX Disable	Disable		2.0		V _{CC} +0.3	V
	Enable		0		0.8	
TX Fault	Fault		2.0		V _{CC} +0.3	V
	Normal		0		0.8	
TX Disable Assert Time		t _{off}			10	us
Receiver						
Centre Wavelength		λ _C	1260		1280	nm
Sensitivity ^{*note4}		P _{IN}			-14	dBm
Receiver Overload		P _{MAX}	0.5			dBm
Output Differential Impedance		P _{IN}	90	100	110	Ω
LOS De-Assert		LOS _D			-18	dBm
LOS Assert		LOS _A	-30			dBm
LOS	High		2.0		V _{CC} +0.3	V
	Low		0		0.8	

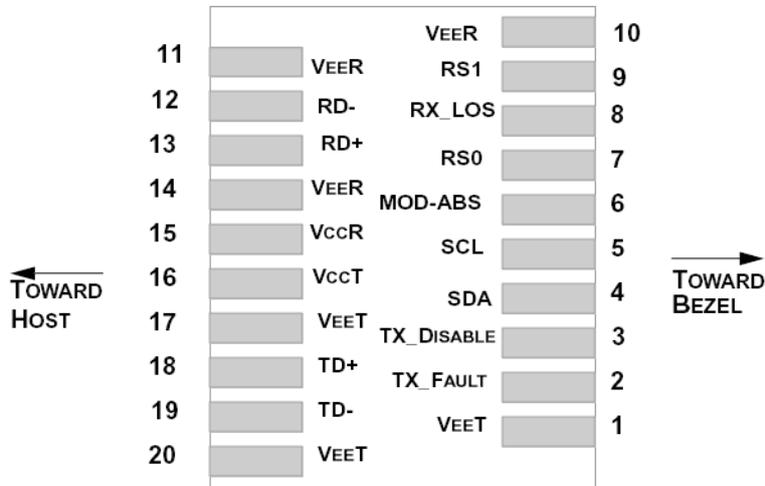
*Note3: Output is coupled into a 9/125um SMF.

*Note4: Measured with worst ER, BER less than 1E-12 and PRBS 2⁷-1 at 6.25Gbps.

SFP+ Transceiver Electrical Pad Layout



Pin Function Definitions



Pin Num.	Name	FUNCTION	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	Note 5
2	TX Fault	Transmitter Fault Indication	3	Note 1
3	TX Disable	Transmitter Disable	3	Note 2, Module disables on high or open
4	SDA	Module Definition 2	3	Data line for Serial ID.
5	SCL	Module Definition 1	3	Clock line for Serial ID.
6	MOD_ABS	Module Definition 0	3	Note 3
7	RS0	RX Rate Select (LVTTTL).	3	This pin has an internal 30k pull down to ground. A signal on this pin will not affect module performance.
8	LOS	Loss of Signal	3	Note 4
9	RS1	TX Rate Select (LVTTTL).	1	This pin has an internal 30k pull down to ground. A signal on this pin will not affect module performance.
10	VeeR	Receiver Ground	1	Note 5
11	VeeR	Receiver Ground	1	Note 5
12	RD-	Inv. Received Data Out	3	Note 6
13	RD+	Received Data Out	3	Note 6
14	VeeR	Receiver Ground	1	Note 5
15	VccR	Receiver Power	2	3.3V ± 5%, Note 7
16	VccT	Transmitter Power	2	3.3V ± 5%, Note 7
17	VeeT	Transmitter Ground	1	Note 5
18	TD+	Transmit Data In	3	Note 8
19	TD-	Inv. Transmit Data In	3	Note 8
20	VeeT	Transmitter Ground	1	Note 5

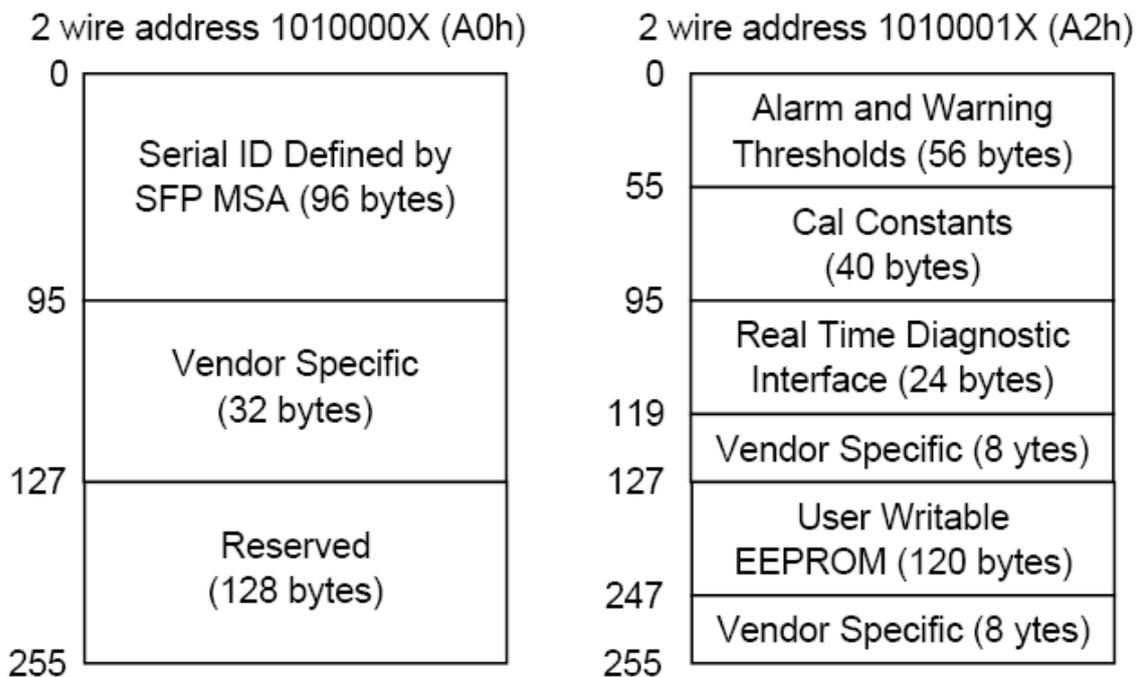
Notes:

- 1) TX Fault is an open collector/drain output, which should be pulled up with a 4.7K – 10K Ω resistor on the host board. Pull up voltage between 2.0V and $V_{ccT/R}+0.3V$. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to $< 0.8V$.
- 2) TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7K – 10 K Ω resistor. Its states are:
Low (0 – 0.8V): Transmitter on
($>0.8, < 2.0V$): Undefined
High (2.0 – 3.465V): Transmitter Disabled
Open: Transmitter Disabled
- 3) Module Absent, connected to VeeT or VeeR in the module.
- 4) LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10K Ω resistor. Pull up voltage between 2.0V and $V_{ccT/R}+0.3V$. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to $< 0.8V$.
- 5) VeeR and VeeT may be internally connected within the SFP+ module.
- 6) RD-/+ : These are the differential receiver outputs. They are AC coupled 100 Ω differential lines which should be terminated with 100 Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 350 and 700 mV differential (175 –350 mV single ended) when properly terminated.
- 7) VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V $\pm 5\%$ at the SFP+ connector pin. Maximum supply current is 300mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 ohm should be used in order to maintain the required voltage at the SFP+ input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP+ transceiver module will result in an inrush current of no more than 30mA greater than the steady state value. VccR and VccT may be internally connected within the SFP+ transceiver module.
- 8) TD-/+ : These are the differential transmitter inputs. They are AC-coupled, differential lines with 100 Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 150 – 1200 mV (75 – 600mV single-ended).

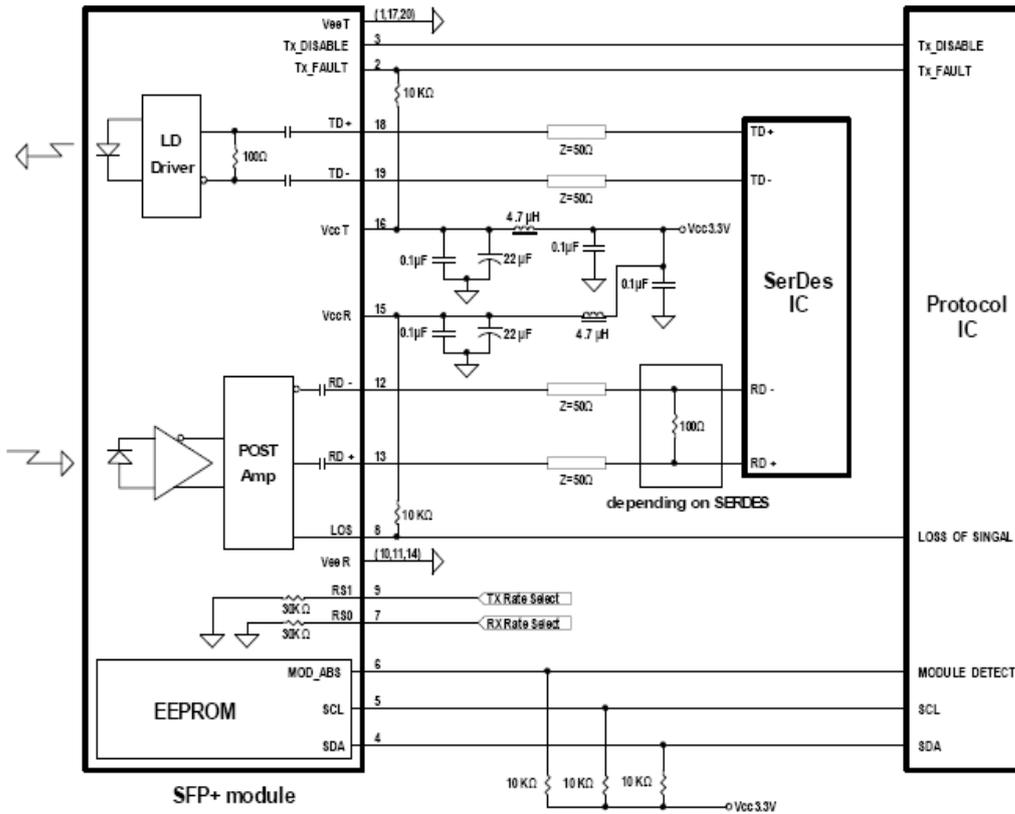
EEPROM

The serial interface uses the 2-wire serial CMOS EEPROM protocol defined for the ATMEL AT24C02/04 family of components. When the serial protocol is activated, the host generates the serial clock signal (SCL). The positive edge clocks data into those segments of the EEPROM that are not writing protected within the SFP+ transceiver. The negative edge clocks data from the SFP+ transceiver. The serial data signal (SDA) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

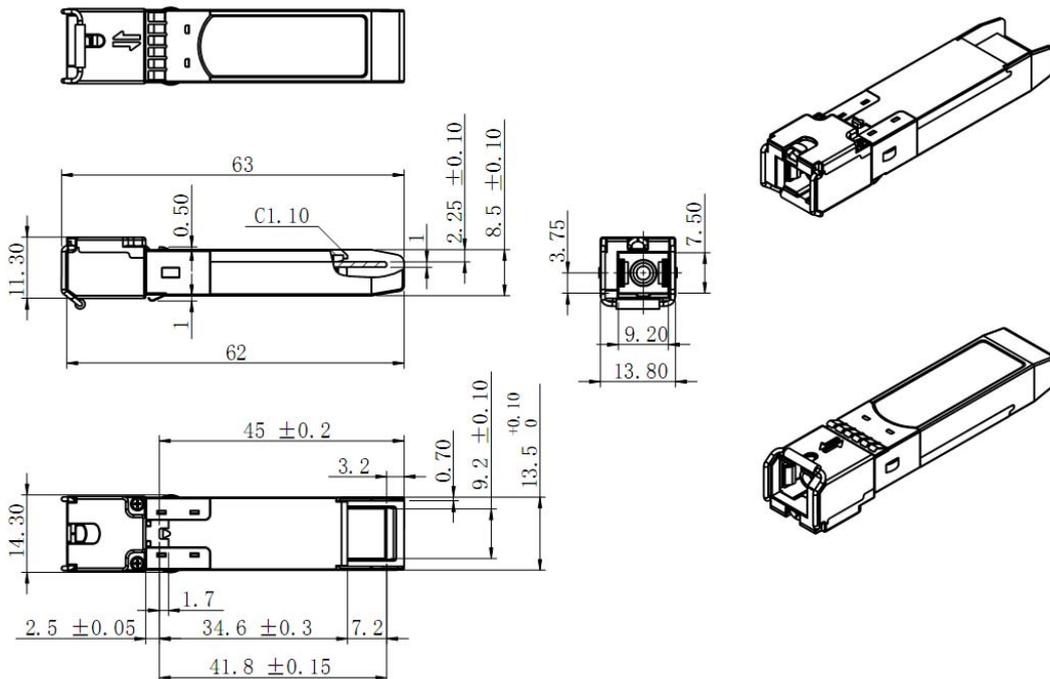
The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. If the module is defined as external calibrated, the diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2h. The digital diagnostic memory map specific data field define as following .For detail EEPROM information, please refer to the related document of SFF 8472 Rev 10.2.



Recommend Circuit Schematic



Mechanical Specifications



Eye Safety

This single-mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

Obtaining Document

You can visit our website: <http://www.eoptolink.com>

Or contact Eoptolink Technology Inc., Ltd. Listed at the end of the documentation to get the latest document.

Revision History

Revision	Initiated	Reviewed	Approved	DCN	Release Date
V1.a	Picard	Frank/Fing		Preliminary	Mar 24, 2014

Notice:

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