

EOLP-1596-40-MN Series

1550nm SFP+ single-Mode Transceiver, With Diagnostic Monitoring
Duplex SFP+ Transceiver, RoHS 6 Compliant

Features

- ◆ Data rate selectable for 1.25G or 9.95~10.3G
- ◆ 1550nm EML Transmitter
- ◆ Distance up to 40km over SMF
- ◆ Single 3.3V Power supply and TTL Logic Interface
- ◆ Duplex LC Connector Interface
- ◆ Hot Pluggable
- ◆ Power Dissipation < 1.5 W (Typical < 1W)
- ◆ Dispersion Tolerance 800ps/nm
- ◆ Operating Case Temperature
Standard: 0°C~+70°C
- ◆ Compliant with SFF-8431 MSA
- ◆ Compliant with SFF-8432 MSA
- ◆ Compliant with SFF-8472 MSA



Applications

- ◆ 10GBASE-ER/EW
- ◆ 1000BASE-EX 1G Ethernet
- ◆ Other optical links

Ordering information

Part No.	Data Rate	Laser	Temp.	Distance	Optical Interface	DD MI
EOLP-1596-40-MN* <small>Note1</small>	9.95Gbps to 10.3Gbps	1550nm EML	Standard	40km	LC	YES
	1.25 Gbps					

Note1: Standard version

*The product image only for reference purpose.

Regulatory Compliance*

Product Certificate	Certificate Number	Applicable Standard
TUV	R50135086	EN 60950-1:2006+A11+A1+A12+A2
		EN 60825-1:2014
		EN 60825-2:2004+A1+A2
UL	E317337	UL 60950-1
		CSA C22.2 No. 60950-1-07
EMC CE	AE 50285865 0001	EN 55022:2010
		EN 55024:2010
FCC	WTF14F0514417E	47 CFR PART 15 OCT., 2013
FDA	/	CDRH 1040.10
ROHS	/	2011/65/EU

* The above certificate number updated to June 2014, because some certificate will be updated every year, such as FDA and ROHS. For the latest certification information, please check with Eoptolink.

Product Description

The EOLP-1596-40-MN series single mode transceiver is small form factor pluggable module for duplex optical data communications up to 10G. It is with the SFP+ 20-pin connector to allow hot plug capability.

This module is designed for single mode fiber and operates at a nominal wavelength of 1550 nm. The transmitter section uses a 1550nm EML, which is class 1 laser compliant according to International Safety Standard IEC-60825.

The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

Absolute Maximum Ratings* ^{Note2}

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	T _s	-40	+85	°C
Supply Voltage	V _{cc}	-0.5	3.6	V

Note2: Exceeding any one of these values may destroy the device permanently.

Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit
Operating Case Temperature	T _c	0		+70	°C
Power Supply Voltage	V _{cc}	3.15	3.3	3.45	V
Power Supply Current	I _{cc}			455	mA
Surge Current	I _{surge}			+30	mA
Baud Rate	10GBASE-ER		10.3		Gbit/s
	10GBASE-EW		9.95		
	1000BASE-EX		1.25		

Performance Specifications – Electrical

Parameter	Symbol	Min.	Typ.	Max	Unit	Notes
Transmitter						
CML Inputs(Differential)	Vin	150		1200	mVpp	AC coupled inputs
Input Impedance (Differential)	Zin	85	100	115	ohms	Rin > 100 kohms @ DC
Tx_DISABLE Input Voltage – High		2		Vcc+0.3	V	
Tx_DISABLE Input Voltage – Low		0		0.8	V	
Tx_FAULT Output Voltage – High		2		Vcc+0.3	V	Io = 400µA; Host Vcc
Tx_FAULT Output Voltage – Low		0		0.5	V	Io = -4.0Ma
Receiver						
CML Outputs (Differential)	Vout	350		700	mVpp	AC coupled outputs
Output Impedance (Differential)	Zout	85	100	115	ohms	
Rx_LOS Output Voltage – High		2		Vcc+0.3	V	Io = 400µA; Host Vcc
Rx_LOS Output Voltage – Low		0		0.8	V	Io = -4.0Ma
MOD_DEF (2:0)	VoH	2.5			V	With Serial ID
	VoL	0		0.5	V	

Performance Specifications – Optical

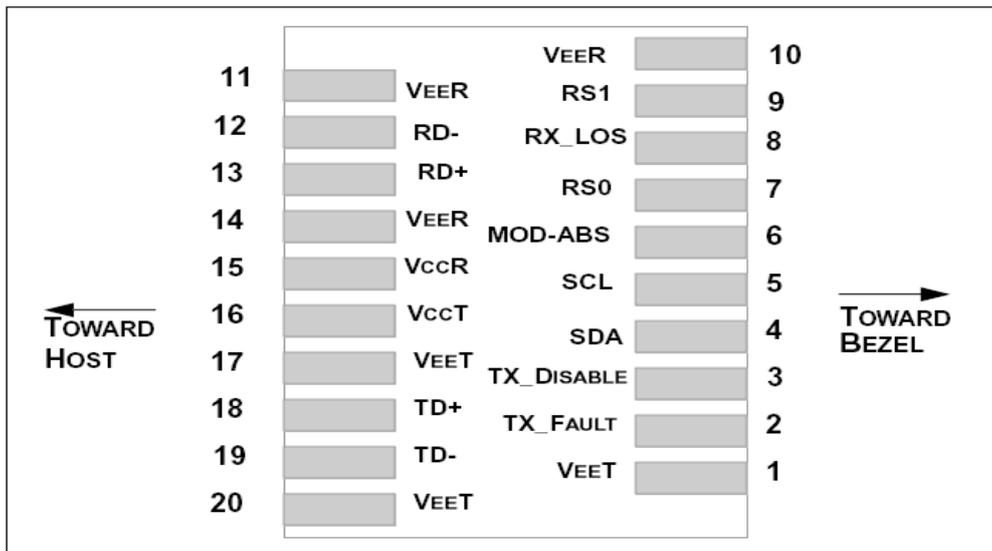
Parameter	Symbol	Min.	Typical	Max.	Unit
9µm Core Diameter SMF			40		Km
Transmitter					
Centre Wavelength	λc	1480	1550	1600	nm
Spectral Width (-20dB)	Δλ			1	nm
Average Output Power*note3	Pout@10.3G	-4.7	-1	4	dBm
	Pout@1.25G	-5		0	
Optical Modulation Amplitude	Pout,OMA	1			dBm
Extinction Ratio	ER@10.3G	3.5			dB
	ER@1.25G	9			
Side Mode Suppression Ratio	SMSR	30			dB
Transmitter and Dispersion Penalty	TDP			2	dB
Average Power of OFF Transmitter				-30	dBm

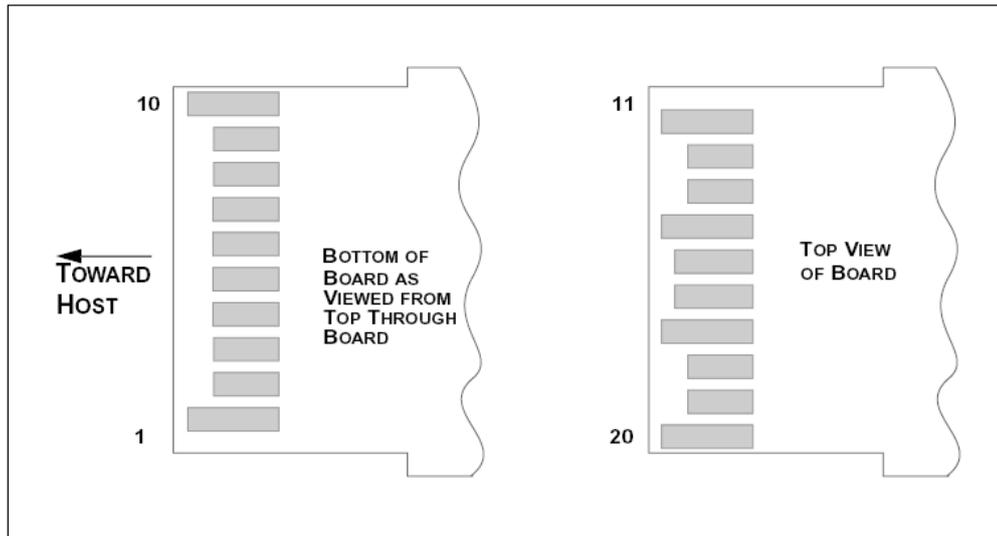
Relative Intensity Noise	RIN			-128	dB/Hz
Input Differential Impedance	Z _{IN}	90	100	110	Ω
TX Disable Assert Time	t _{off}			10	us
Receiver					
Centre Wavelength	λ _C	1260		1600	nm
Sensitivity *note4	P _{min@10.3G}			-15.8	dBm
	P _{min@1.25G}			-24	
Receiver Overload	P _{MAX}	-1			dBm
Output Differential Impedance	P _{IN}	90	100	110	Ω
LOS De-Assert	LOS _{D@10.3G}			-16.5	dBm
	LOS _{D@1.25G}			-26	
LOS Assert	LOS _{A@10.3G}	-30			dBm
	LOS _{A@1.25G}	-38			

Note3: Output is coupled into a 9/125um SMF. The -4.7dBm is reference IEEE 802.3ae, the typical value is -1dBm.

Note4: Minimum average optical power measured at the BER less than 1E-12, back to back. The measure pattern is PRBS 2³¹-1.

SFP+ Transceiver Electrical Pad Layout





Pin Function Definitions

Pin Num.	Name	FUNCTION	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	Note 5
2	TX Fault	Transmitter Fault Indication	3	Note 1
3	TX Disable	Transmitter Disable	3	Note 2, Module disables on high or open
4	SDA	Module Definition 2	3	2-wire Serial Interface Data Line.
5	SCL	Module Definition 1	3	2-wire Serial Interface Clock.
6	MOD_ABS	Module Definition 0	3	Note 3
7	RS0	RX Rate Select (LVTTTL).	3	RS0 for Rate Select: Open or Low=Module supports 1.25Gb/s; High=Module supports 9.95Gb/s to 10.3125Gb/s
8	LOS	Loss of Signal	3	Note 4
9	RS1	TX Rate Select (LVTTTL).	1	No connection required
10	VeeR	Receiver Ground	1	Note 5
11	VeeR	Receiver Ground	1	Note 5
12	RD-	Inv. Received Data Out	3	Note 6
13	RD+	Received Data Out	3	Note 6

14	VeeR	Receiver Ground	1	Note 5
15	VccR	Receiver Power	2	3.3V ± 5%, Note 7
16	VccT	Transmitter Power	2	3.3V ± 5%, Note 7
17	VeeT	Transmitter Ground	1	Note 5
18	TD+	Transmit Data In	3	Note 8
19	TD-	Inv. Transmit Data In	3	Note 8
20	VeeT	Transmitter Ground	1	Note 5

Notes:

1) TX Fault is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor on the host board. Pull up voltage between 2.0V and VccT/R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

2) TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7K – 10 KΩ resistor. Its states are:

Low (0 – 0.8V): Transmitter on

(>0.8, < 2.0V): Undefined

High (2.0 – 3.465V): Transmitter Disabled

Open: Transmitter Disabled

3) Module Absent, connected to VeeT or VeeR in the module.

4) LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor. Pull up voltage between 2.0V and VccT/R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

5) The module signal ground contacts, VeeR and VeeT, should be isolated from the module case.

6) RD-/+ : These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 350 and 700 Mv differential (175 –350 Mv single ended) when properly terminated.

7) VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V ±5% at the SFP+ connector pin. Maximum supply current is 455mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 ohm should be used in order to maintain the required voltage at the SFP+ input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP+ transceiver module will result in an inrush current of no more than 30Ma greater than the steady state value. VccR and VccT may be internally connected within the SFP+ transceiver module.

8) TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 150 – 1200 Mv (75 – 600Mv single-ended).

Rate Select Control

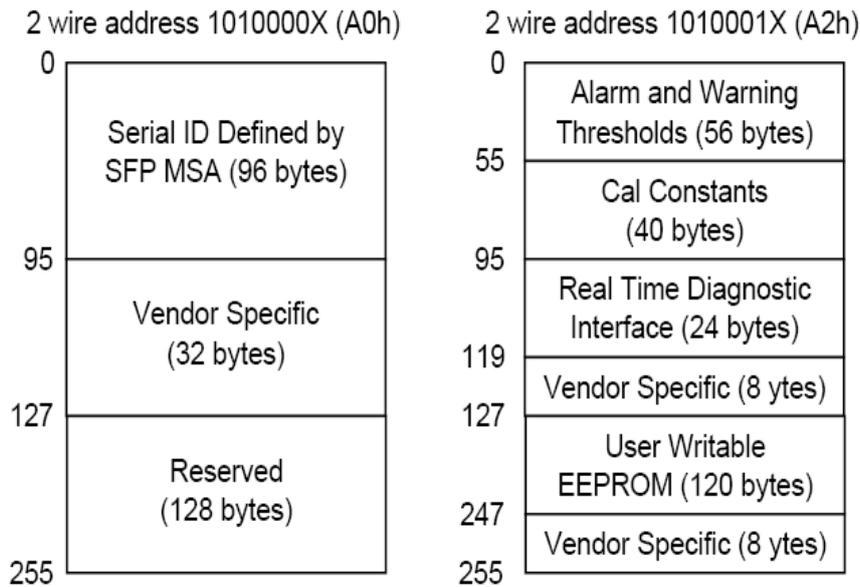
Transceiver data rate selected through the 2-wire bus in accordance with SFF-8472 Rev.10.3. Soft RS0 is set at Bit 3, Byte 110, Address A2h. Soft RS0 default state on power up is '0' LOW, and the state is reset following a power cycle. Writing '1' HIGH selects max. data rate operation. Transceiver data rate is the logic OR of the input state of the RS0 pin and soft RS0 bit. Thus, if either the RS0 pin OR the soft RS0 bit is HIGH then selected data rate will be 9.95 and 10.3Gb/s. Conversely, to select data rate 1.25Gb/s both the RS0 pin and the soft RS0 bit are set LOW.

Parameter	State	Conditions
RS0	Low	RX data rate is selected as 1.25Gbps
	High	RX data rate is selected as 9.95~10.3Gbps
RS1	NC	This pin is not used

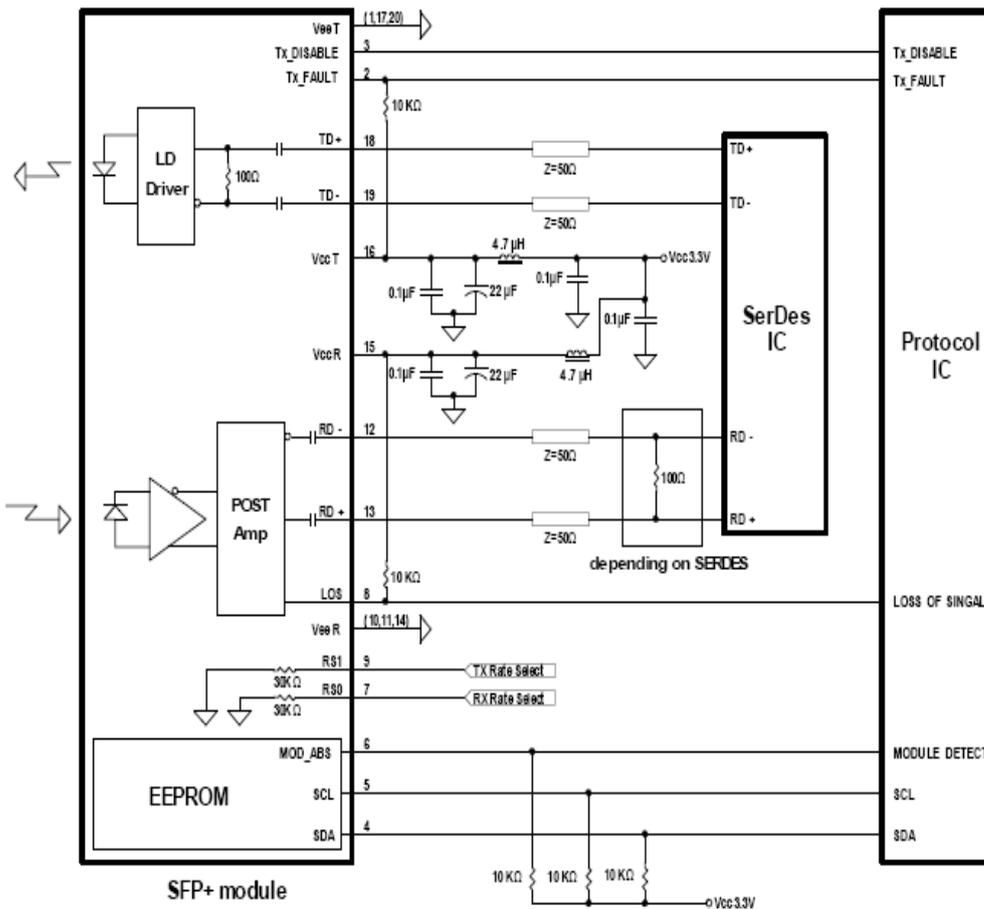
EEPROM

The serial interface uses the 2-wire serial CMOS EEPROM protocol defined for the ATMEL AT24C02/04 family of components. When the serial protocol is activated, the host generates the serial clock signal (SCL). The positive edge clocks data into those segments of the EEPROM that are not writing protected within the SFP+ transceiver. The negative edge clocks data from the SFP+ transceiver. The serial data signal (SDA) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

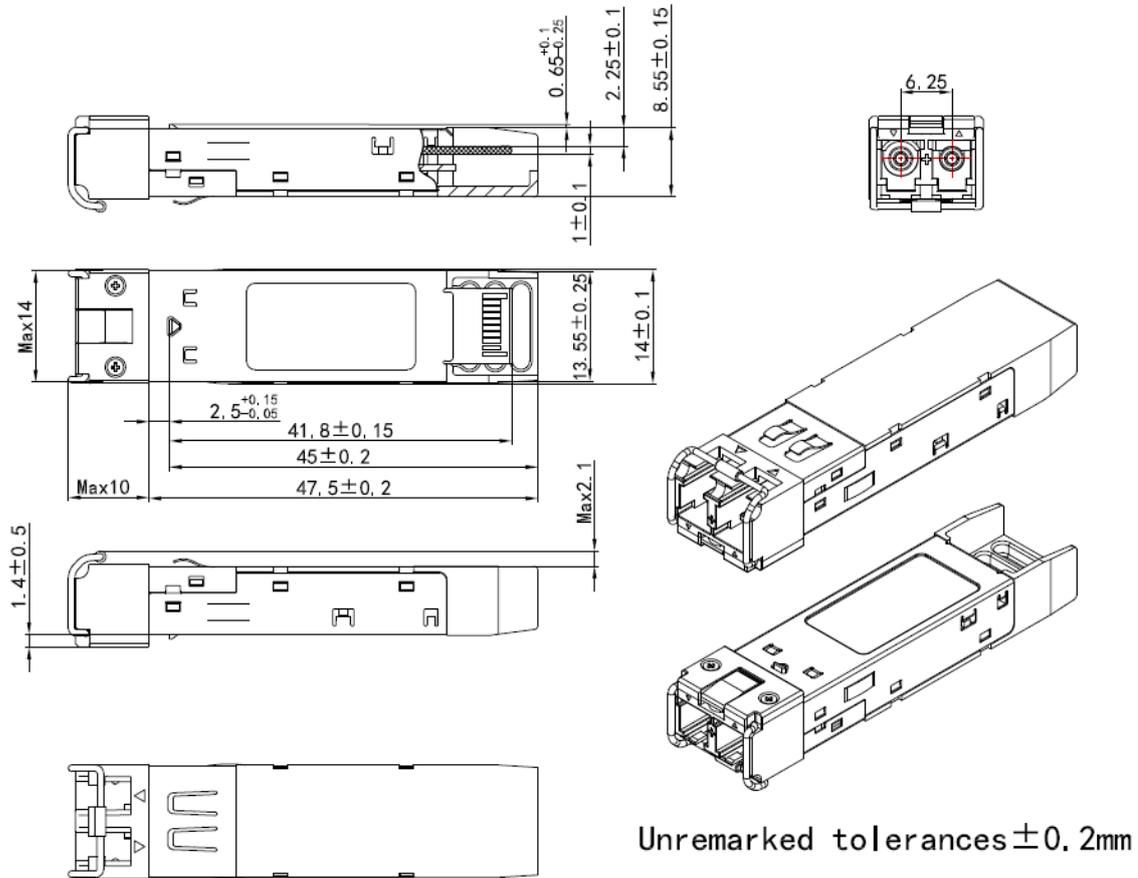
The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. If the module is defined as external calibrated, the diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2h. The digital diagnostic memory map specific data field define as following .For detail EEPROM information, please refer to the related document of SFF 8472 Rev 10.2.



Recommend Circuit Schematic



Mechanical Specifications



*This 2D drawing only for reference, please check with Eoptolink before ordering.

Eye Safety

This single-mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

Obtaining Document

You can visit our website:

<http://www.eoptolink.com>

Or contact Eoptolink Technology Inc., Ltd. Listed at the end of the documentation to get the latest document.

Revision History

Revision	Initiated	Reviewed	Approved	DCN	Release Date
V1.a	Abby/Eason	Kelly/Fing		New Released.	Mar 13, 2014
V1.b	Abby	Kelly/Vina		Update PN, Regulatory Compliance and Mechanical Specifications	Feb 5, 2015
V1.c	Angela	Kelly/William/ Chao.Wang		Update the regulatory compliance, picture, 2D drawing and contact.	March 22, 2018

Notice:

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Contact:

Add: No.127 West Wulian Street, Gongxing Town, Shuangliu district, Chengdu City, Sichuan, China.

Tel: (+86) 028-67087999

Fax: (+86) 28-67087979-8010

Postal: 610213

E-mail:sales@eoptolink.com

<http://www.eoptolink.com>