

EOLS-1531-X-D Series

Single-Mode 1550nm 3.072/2.4576Gb/s Duplex SFP Transceiver RoHS OBSAI/CPRI Compliant

Features

- ◆ Operating Data Rate up to 3.072Gbps
- ◆ 1550nm DFB Laser Transmitter
- ◆ Up to 40km/80km with 9µm fiber for 3.072Gb/s
- ◆ Up to 40km/80km with 9µm fiber for 2.4576Gb/s
- ◆ Single 3.3V Power Supply and TTL Logic Interface
- Hot-Pluggable SFP Footprint Duplex LC Connector
 Interface
- ◆ Class 1 FDA and IEC60825-1 Laser Safety Compliant
- Operating Case Temperature

Standard: 0°C~+70°C

Industrial: -40°C to 85°C

- Compliant with SFP MSA
- ◆ Compliant with SFF-8472



Applications

- Gigabit Ethernet Interface, Fiber channel
- Wireless and cellular base station
 system interconnect
- OBSAI rates: 3.072 Gb/s, 1.536
 Gb/s, 0.768 Gb/s
- CPRI rates: 3.072 Gb/s, 2.4576
 Gb/s, 1.2288Gb/s, 0.6144Gb/s
- Other Optical Links

Ordering Information

Part No.	Data Rate	Fiber	Distance *Note2	Interface	Temp.	DDMI
EOLS-1531-40-D*Note1	3.072Gbps	SMF	40km	LC	Standard	YES
EOLS-1531-40-DI	3.072Gbps	SMF	40km	LC	Industrial	YES
EOLS-1531-80-D*Note1	3.072Gbps	SMF	80km	LC	Standard	YES
EOLS-1531-80-DI	3.072Gbps	SMF	80km	LC	Industrial	YES

Note1: Standard version

Note2: 40 km/80 km with $9/125 \mu \text{m}$ SMF under 3.072 Gbps.



Regulatory Compliance*Note3

Product Certificate	Certificate Number	Applicable Standard
		EN 60950-1:2006+A11+A1+A12+A2
TUV	R50135086	EN 60825-1:2014
		EN 60825-2:2004+A1+A2
UL	F247227	UL 60950-1
UL	E317337	CSA C22.2 No. 60950-1-07
EMC CE	A F 60005065 0004	EN 55022:2010
EMC CE	AE 50285865 0001	EN 55024:2010
FCC	WTF14F0514417E	47 CFR PART 15 OCT., 2013
FDA	1	CDRH 1040.10
ROHS	1	2011/65/ĘU

Note3: The above certificate number updated to June 2014, because some certificate will be updated every year, such as FDA and ROHS. For the latest certification information, please check with Eoptolink.

Product Description

The EOLS-1531-X-D series single mode transceiver is small form factor pluggable module for serial optical data communications. It is with the SFP 20-pin connector to allow hot plug capability. This module is designed for single mode fiber and operates at a nominal wavelength of 1550nm.

The transmitter section uses a multiple quantum well 1550nm DFB laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

The EOLS-1531-X-D series are designed to be compliant with SFF-8472.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	Ts	-40	+85	°C
Supply Voltage	Vcc	-0.5	3.6	V
Operating Relative Humidity	RH	5	85	%

Recommended Operating Conditions

Parameter	Symbol		Min.	Typical	Max	Unit
Operating Case Temperature	Tc	EOLS-1531-X-D	0		+70	°C
Operating Case Temperature		EOLS-1531-X-DI	-40		+85	°C
Power Supply Voltage	Vcc		3.15	3.3	3.45	V
Module supply current	Icc				350	mA
Data Rate			0.614		3.072	Gbps



Performance Specifications – Electrical

Para	meter	Symbol	Min.	Тур.	Max	Unit	Notes		
	Transmitter								
	ML vifferential)	Vin	400		1600	mVpp	AC coupled inputs*Note4		
Input Im (Differen		Zin	85	100	115	Ohms	Rin>100 ohms@DC		
TV Die	Disable		2		Vcc	V			
TX_Dis	Enable		0		0.8	V			
TV FALLS	_ Fault		2		Vcc+0.3				
TX_FAUL	Normal		0		0.6	V			
			Rece	eiver		1			
	Outputs rential)	Vout	400	800	1200	mVpp	AC coupled outputs*Note5		
1	npedance rential)	Zout	85	100	115	ohm			
DV LOC	LOS		2		Vcc+0.3	V			
RX_LOS -	Normal		0		0.6	V			
MOD	EE (0:2)	VoH \	2		Vcc	V	With Serial ID		
ט_טואו	EF (0:2)	VoL	0		0.8	V	wiiii Senai iD		

Performance Specifications – Optical

(1550nm DFB and PIN/TIA, 40km)

Parameter	Symbol	Min.	Typical	Max.	Unit
9µm Core Diameter SMF	L		40		km
Data Rate			3.072		Gbps
Transr	nitter				
Center Wavelength	λ _C	1500	1550	1600	nm
Spectral Width (-20dB)	Δλ			1	nm
Average Output Power*(note6)	Pout	0		5	dBm
Extinction Ratio*(note7)	ER	5	7		dB
Rise/Fall Time(20%~80%)	tr/tf			100	ps
Output Optical Eye*(note7)	FC PI-4 REV 8.0 Compliant*(note9)				
TX_Disable Assert Time	t_off			10	us
Pout@TX Disable Asserted	P _{OFF}			-35	dBm
Rece	iver				
Center Wavelength	λc	1260		1600	nm
Receiver Sensitivity*(note8)	Pmin			-18	dBm
Receiver Overload	Pmax	-3			dBm
Link Penalty	PL			1	dB
LOS De-Assert	LOSD			-19	dBm



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LOS Assert	LOSA	-31		dBm
LOS Hysteresis*(note10)		0.5		dB

(1550nm DFB and APD/TIA, 80km)

Parameter	Symbol	Min.	Typical	Max.	Unit
9µm Core Diameter SMF	L		80		km
Data Rate			3.072		Gbps
Transr	nitter				
Center Wavelength	λ _C	1500	1550	1600	nm
Spectral Width (-20dB)	Δλ			1	nm
Average Output Power*(note6)	Pout	0		5	dBm
Extinction Ratio*(note7)	ER	5	7		dB
Rise/Fall Time(20%~80%)	tr/tf			100	ps
Output Optical Eye*(note7)	FC PI-4 REV 8.0 Compliant*(note9)				
TX_Disable Assert Time	t_off	,	/	10	us
Pout@TX Disable Asserted	Poff			-35	dBm
Rece	iver				
Center Wavelength	λc	1260		1600	nm
Receiver Sensitivity*(note8)	Pmin			-27	dBm
Receiver Overload	Pmax	-10			dBm
Link Penalty	PL			1	dB
LOS De-Assert	LOSD			-28	dBm
LOS Assert	LOSA	-42			dBm
LOS Hysteresis*(note10)		0.5			dB

Note4: internally AC coupled, and terminated to 100Ω differential load.

Note5: AC-coupled CML logic.

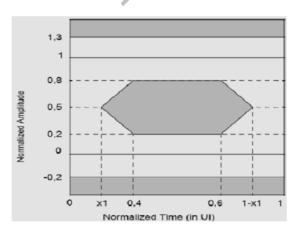
Note6: Output is coupled into a 9/125µm single-mode fiber.

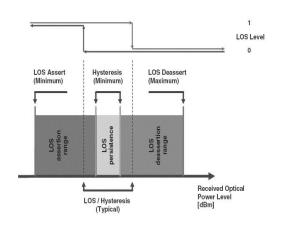
Note7: Filtered, measured with a PRBS 2⁷-1 test pattern @3.1Gbps.

Note8: Minimum average optical power measured at BER less than 1E-12, with a 2⁷-1 PRBS.

Note10: LOS Hysteresis

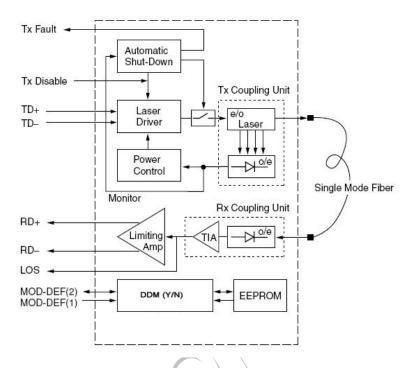
Note9: Eye Pattern Mask



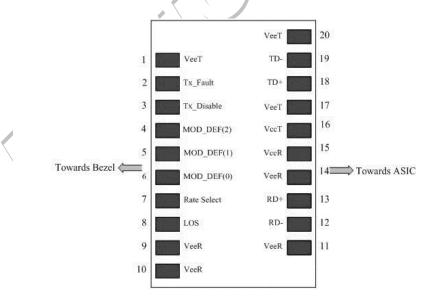


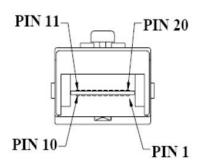


Functional Description of Transceiver



SFP Transceiver Electrical Pad Layout





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Pin Function Definitions

Pin Num.	Name	Function	Plug Seq.	Notes		
1	VeeT	Transmitter Ground	1	5)		
2	TX Fault	Transmitter Fault Indication	3	1)		
3	TX Disable	Transmitter Disable	3	2) Module disables on high or open		
4	MOD-DEF2	Module Definition 2	3	3) Data line for Serial ID.		
5	MOD-DEF1	Module Definition 1	3	3) Clock line for Serial ID.		
6	MOD-DEF0	Module Definition 0	3	3) Grounded within the module.		
7	Rate Select	Not Connect	3	Function not available		
8	LOS	Loss of Signal	3	4)		
9	VeeR	Receiver Ground	1	5)		
10	VeeR	Receiver Ground	\ 1 \	5)		
11	VeeR	Receiver Ground	1	5)		
12	RD-	Inv. Received Data Out	3	6)		
13	RD+	Received Data Out	3	6)		
14	VeeR	Receiver Ground	1	5)		
15	VccR	Receiver Power	2	7) 3.3 ± 5%		
16	VccT	Transmitter Power	2	7) 3.3 ± 5%		
17	VeeT	Transmitter Ground	1	5)		
18	TD+	Transmit Data In	3	8)		
19	TD-	Inv. Transmit Data In	3	8)		
20	VeeT	Transmitter Ground	1	5)		

Notes:

- 1) TX Fault is an open collector/drain output, which should be pulled up with a $4.7K-10K\Omega$ resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
- 2) TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a $4.7 10 \text{ K}\Omega$ resistor. Its states are:

Low (0 - 0.8V): Transmitter on (>0.8, < 2.0V): Undefined

High (2.0 - 3.465V): Transmitter Disabled

Open: Transmitter Disabled

- 3) Modulation Absent, connected to VEET or VEER in the module.
- 4) LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K 10KΩ resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates





the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

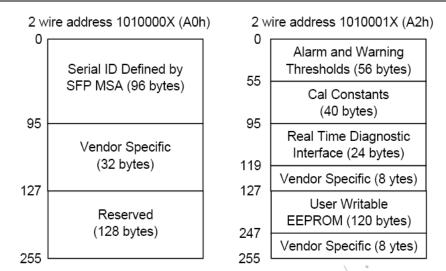
- 5) VeeR and VeeT may be internally connected within the SFP module.
- 6) RD-/+: These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 400 and 2000Mv differential (200 –1000Mv single ended) when properly terminated.
- 7) VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V ±5% at the SFP connector pin. Maximum supply current is 350mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 10hm should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30Ma greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.
- 8) TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 400 2000Mv (200 1000Mv single-ended).

FFPROM

The serial interface uses the 2-wire serial CMOS EEPROM protocol defined for the ATMEL AT24C02/04 family of components. When the serial protocol is activated, the host generates the serial clock signal (SCL). The positive edge clocks data into those segments of the EEPROM that are not write protected within the SFP transceiver. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. If the module is defined as external calibrated, the diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2H. The digital diagnostic memory map specific data field define as following .For detail EEPROM information, please refer to the related document of SFF 8472 Rev 9.3.





EEPROM Serial ID Memory Contents

Accessing Serial ID Memory uses the 2 wire address 1010000X(A0H). Memory Contents of Serial ID are shown in Table 1.

Table 1 Serial ID Memory Contents

Addr.	Size (Bytes)	Name of Field	Hex	Description					
	BASE ID FIELDS								
0	1	Identifier	03	SFP					
1	1	Ext. Identifier	04	SFP function is defined by					
Į.	1	Ext. Identifier	04	serial ID only					
2	1	Connector	07	LC Connector					
3-10	8	Transceiver	00 00 00 02 10 10 01	Transmitter Code 40km					
3-10	0	Transceiver	04	Transmitter Code 40km					
3-10	8	Transceiver	00 00 00 02 80 10 01	Transmitter Code 80km					
3-10	ď	Transceiver	04	Transmitter Code ookin					
11	1	Encoding	01	Compatible with 8B/10B					
11	'	Litodaling	01	encoded data					
12	1	BR, Nominal	1F	3.072Gbps					
13	1	Reserved	00						
14	1	Length (9µm)km	28/50	40 km/80km of single mode i					
14	ı	Lengin (apm)km	20/30	ber @ 3.1GBit/sec					
15	1	Length(9µm)100m	FF	40 km/80km of single mode i					
13	I	Lengin(apin) room	1 1	ber @ 3.1GBit/sec					
16	1	Length (50µm) 10m	00						
17	1	Length(62.5µm)10m	00						
18	1	Length (Copper)	00						
19	1	Reserved	00						



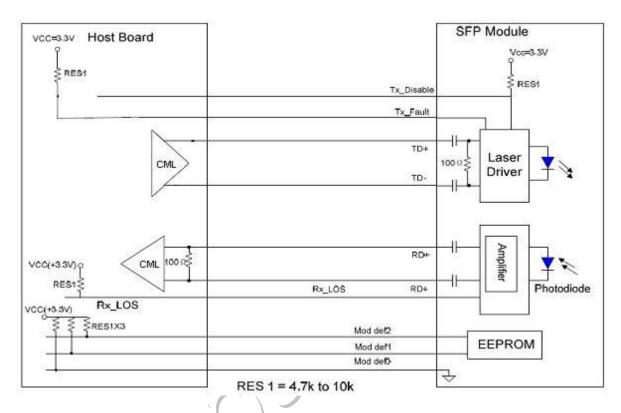
SFP Series

			XX XX XX XX XX XX	
20-35	16	Vendor name	XX XX XX XX XX XX	Vendor name (ASCII)
			XX XX XX XX (note11)	
36	1	Reserved	00	
37-39	3	Vendor OUI	XX XX XX ^(note11)	
			XX XX XX XX XX XX	
40-55	16	Vendor PN	XX XX XX XX XX XX	Transceiver part number
			XX XX XX XX	
56-59	4	Vendor rev	XX XX XX XX(note11)	
60-61	2	Wavelength	060E	1550nm
62	1	Reserved	00	
63	1	CC_BASE	Check Sum \	Check code for Base ID
03	l	CC_BASE	(Variable)	Fields
		EXTEND	ED ID FIELDS	
				TX_DISABLE, TX_FAULT
64-65	2	Options	00 1A	and Loss of Signal
				implemented.
66	1	BR, max	00	
67	1	BR, min	00	
		\ \ \	XX XX XX XX XX XX	Serial Number of transceiver
68-83	16	Vendor SN	XX XX 20 20 20 20	(ASCII). For example
			20 20 20 20 ^(note11)	"B000822".
84-91	8	Date code	XX XX XX XX XX XX	Manufactory date code.
04-91	0	Date code	XX XX ^(note11)	For example "080405".
92	1	Diagnostic	XX ^(note11)	Digital diagnostic monitoring
92	1	Monitoring Type		implemented
93	1	Enhanced Options	XX ^(note11)	Optional flags
94	1	SFF_8472	01	SFF-8472 Compliance to
34		Compliance	O1	revision 9.3
95	1	CC_EXT	Check Sum	Check sum for Extended ID
93	1//	CC_LX1	(Variable)	Field.
		VENDOR SP	ECIFIC ID FIELDS	
96-127	32	Vendor Specific	Read only	Depends on customer
30-121	32	vendor opeding	Nead Offig	information
128-255	128	Reserved	Read only	

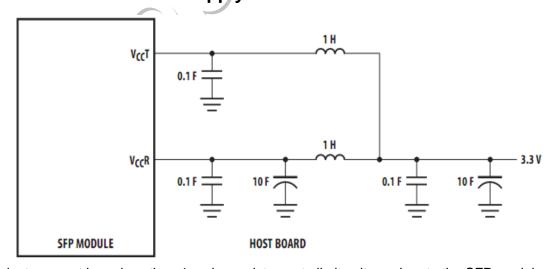
Note11: The "XX" byte should be filled in according to practical case. For more information, please refer to the related document of SFP Multi-Source Agreement (MSA).



Recommend Circuit Schematic



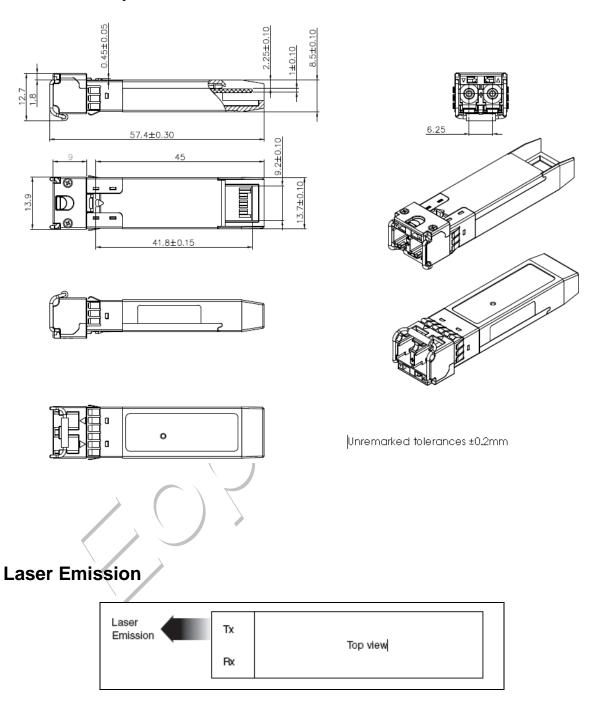
Recommended Power Supply Filter



^{*} Inductors must have less than 1 series resistance to limit voltage drop to the SFP module



Mechanical Specifications



Obtaining Document

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Revision History

Revision	Initiated	Reviewed	Approved	DCN	Release Date
V1.a	Oliver/Yi.Wan	Lyn/Kelly/Jason		New Released.	Mar 10, 2017

Notice:

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